

# Collapsible Pushdown Automata and Recursion Schemes

MATTHEW HAGUE, Royal Holloway, University of London

ANDRZEJ S. MURAWSKI, DIMAP and Department of Computer Science, University of Warwick

C.-H. LUKE ONG, Department of Computer Science, University of Oxford

OLIVIER SERRE, IRIF, CNRS & Université Paris Diderot – Paris 7

We consider recursion schemes (not assumed to be homogeneously typed, and hence not necessarily safe) and use them as generators of (possibly infinite) ranked trees. A recursion scheme is essentially a finite typed deterministic term rewriting system that generates, when one applies the rewriting rules ad infinitum, an infinite tree, called its value tree. A fundamental question is to provide an equivalent description of the trees generated by recursion schemes by a class of machines.

In this paper we answer this open question by introducing collapsible pushdown automata (CPDA), which are an extension of deterministic (higher-order) pushdown automata. A CPDA generates a tree as follows. One considers its transition graph, unfolds it and contracts its silent transitions, which leads to an infinite tree which is finally node labelled thanks to a map from the set of control states of the CPDA to a ranked alphabet.

Our contribution is to prove that these two models, higher-order recursion schemes and collapsible pushdown automata, are equi-expressive for generating infinite ranked trees. This is achieved by giving effective transformations in both directions.

CCS Concepts: • Theory of computation → Lambda calculus; Automata over infinite objects; Grammars and context-free languages; Program schemes;

General Terms: Theory, Rewriting, Lambda-Calculus, Automata

Additional Key Words and Phrases: Higher-Order Recursion Schemes, Higher-Order (Collapsible) Pushdown Automata

ACM Reference Format:

Matthew Hague, Andrzej S. Murawski, C.-H. Luke Ong and Olivier Serre, 2015. Collapsible Pushdown Automata and Recursion Schemes. *ACM Trans. Comput. Logic* V, N, Article A (January YYYY), 43 pages.

DOI: <http://dx.doi.org/10.1145/0000000.0000000>

## 1. INTRODUCTION

This paper establishes the equivalence of two models: higher-order recursion schemes and collapsible pushdown automata. A recursion scheme is a simply-typed term rewriting system. Deterministic recursion schemes can be viewed naturally as generators of possibly infinite trees. Collapsible pushdown automata (CPDA) are an extension of higher-order pushdown automata, and they naturally induce a transition graph. An infinite ranked tree can be constructed by first unfolding such a transition graph and then contracting the silent transitions. Applying this construction to CPDA defines a family of ranked trees which coincides with the family of ranked trees generated from higher-order recursion schemes.

---

This work is supported by...

Permission to make digital or hard copies of all or part of this work for personal or classroom use is granted without fee provided that copies are not made or distributed for profit or commercial advantage and that copies bear this notice and the full citation on the first page. Copyrights for components of this work owned by others than ACM must be honored. Abstracting with credit is permitted. To copy otherwise, or republish, to post on servers or to redistribute to lists, requires prior specific permission and/or a fee. Request permissions from [permissions@acm.org](mailto:permissions@acm.org).

© YYYY ACM. 1529-3785/YYYY/01-ARTA \$15.00

DOI: <http://dx.doi.org/10.1145/0000000.0000000>

### Recursive Applicative Program Schemes

Recursion schemes have a long and rich history<sup>1</sup>. They go back to Nivat’s recursive applicative program schemes [Nivat 1972], which correspond to order-1 recursion schemes in our sense, and to Garland and Luckham’s monadic recursion schemes [Garland and Luckham 1973]. According to Nivat, a recursive applicative program scheme is a finite system of equations of the form  $F_i(x_1, \dots, x_{n_i}) = p_i$ , where each  $x_j$  is an order-0 variable and  $p_i$  is an order-0 term constructed from the non-terminal symbols  $F_i$ , terminal symbols and the variables  $x_1, \dots, x_{n_i}$ . A program is then a program scheme together with an interpretation in some domain. The least fixed point of the function defined by the rewriting rules of a program scheme gives a possibly infinite term tree over the terminals alphabet, known as the value of the program in the free / Hebrand interpretation; applying the interpretation to this infinite term gives the value of the program. Thus the program scheme gives the uninterpreted syntax tree of some functional program that is then fully specified owing to the interpretation. For example, the term  $if(eq(1, 0), 2, 3)$  has the value 3 under the natural interpretation of  $if$ ,  $eq$ , and the natural numbers.

Nivat also introduced a notion of equivalence: two program schemes are equivalent just if they compute the same function under every interpretation. Courcelle and Nivat [Courcelle and Nivat 1978] showed that two program schemes are equivalent if and only if they generate the same infinite term tree, thus underlining the importance of studying the tree generated by a scheme. Following the work of Courcelle [Courcelle 1978a; Courcelle 1978b], the equivalence problem for program schemes is inter-reducible to the problem of language equivalence for deterministic pushdown automata (DPDA). The question of the decidability of the latter was first posed in the 1960s. It was only settled, positively, by Sénizergues in 1997 [Sénizergues 1997; Sénizergues 2002], which therefore also established the decidability of the program scheme equivalence problem.

### Extension of Schemes to Higher Orders

In Nivat’s program scheme, the non-terminals and the variables are restricted to order 1 and 0 respectively. It follows that they are not suited to model higher-order recursive programs. A major theme in the late 1970s was the extension of program schemes to higher orders [Indermark 1976; Damm 1977a; Damm 1977b; Engelfriet and Schmidt 1977; Engelfriet and Schmidt 1978].

In an influential paper [Damm 1982], Damm introduced level- $n$   $\lambda$ -schemes, extending the work of Courcelle and Nivat. Damm’s schemes coincide with the safe fragment of the recursion schemes, which we will define later in the paper. It is important to note that so far there was no known model of automata equi-expressive with Damm’s schemes; in particular, there was no known reduction of the equivalence problem for schemes to a language equivalence problem for (some model of) automata.

Later, Damm and Goerdts [Damm 1982; Damm and Goerdts 1986] considered the word languages generated by level- $n$   $\lambda$ -schemes, and showed that they coincide with a hierarchy introduced earlier by Maslov [Maslov 1974; Maslov 1976]. To define his hierarchy, Maslov introduced higher-order pushdown automata (Higher-order PDA); he also gave an equivalent definition of the hierarchy in terms of higher-order indexed grammars.

### Higher-Order Recursion Schemes as Generators of Infinite Structures

Since the late 1990s, motivated mainly by applications to program verification, there has been a strong and sustained interest in infinite structures that admit finite descriptions; see [Bárány et al. 2011] for an overview. The central question, given a class of such structures, is

<sup>1</sup>De Miranda’s thesis [de Miranda 2006], among others, contains an account of the history. See also [Ong 2015a]

to find the most expressive logic for which model checking is decidable. Of course decidability here is a trade-off between richness of the structure and expressivity of the logic.

Of special interest are tree-like structures. Higher-order PDA as a generating device for possibly infinite labelled ranked trees were first studied by Knapik, Niwiński and Urzyczyn [Knapik et al. 2002]. As in the case of word languages, an infinite hierarchy of trees is defined according to the order of the generating higher-order PDA; lower orders of the hierarchy are well-known classes of trees: orders 0, 1 and 2 are respectively the regular [Rabin 1969], algebraic [Courcelle 1995] and hyperalgebraic trees [Knapik et al. 2001]. Knapik et al. considered another method of generating such trees, namely, by higher-order (deterministic) recursion schemes that satisfy the safety constraint. A major result of their work is the equi-expressivity of both methods as tree generators. In particular it implies that the equivalence problem for higher-order safe recursion schemes is inter-reducible to the problem of language equivalence for deterministic higher-order PDA.

An alternative approach was developed by Caucal [Caucal 2002] who introduced two infinite hierarchies, one consisting of infinite trees and the other of infinite graphs, defined by mutually recursive maps: unfolding which transforms graphs to trees, and inverse rational mapping (or MSO-interpretation [Carayol and Wöhrle 2003]) which transforms trees to graphs. He showed that the tree hierarchy coincides with the trees generated by safe recursion schemes.

However, the fundamental question open since the early 1980s of finding a class of automata that characterises the expressivity of higher-order recursion schemes was left open. Indeed, the results of Damm and Goerdts, as well as those of Knapik et al. may only be viewed as attempts to answer the question as they both had to impose the same syntactic constraints on recursion schemes, called derived types and safety respectively, in order to establish their results.

A partial answer was later obtained by Knapik, Niwiński, Urzyczyn and Walukiewicz. They proved that order-2 homogeneously-typed, but not necessarily safe, recursion schemes are equi-expressive with a variant class of order-2 pushdown automata called panic automata [Knapik et al. 2005].

Finally, we gave a complete answer to the question in an extended abstract [Hague et al. 2008]. For this, we introduced a new kind of higher-order pushdown automata, which generalises pushdown automata with links [Aehlig et al. 2005], or equivalently panic automata, to all finite orders, called collapsible pushdown automata (CPDA), in which every symbol in the stack has a link to a (necessarily lower-ordered) stack situated somewhere below it. A major result of [Hague et al. 2008] and of the present paper is that for every  $n \geq 0$ , order- $n$  recursion schemes and order- $n$  CPDA are equi-expressive as generators of trees.

#### Decidability of Monadic Second Order Logic

This quest of finding an alternative description of those trees generated by recursion schemes was led in parallel with the study of the decidability of the model-checking problem for the monadic second order logic (MSO) and the modal  $\mu$ -calculus (see [Thomas 1997; Arnold and Niwiński 2001; Grädel et al. 2002; Flum et al. 2007] for background about these logics and connections with finite automata and games).

The decidability of the MSO theories of trees generated by safe recursion schemes of all finite orders was established by Knapik, Niwiński and Urzyczyn [Knapik et al. 2002] and independently by Caucal [Caucal 2002] who proved, additionally, the MSO decidability of the associated graph hierarchy. The decidability result was first extended to possibly unsafe recursion schemes of order 2 by Knapik et al. [Knapik et al. 2005] and Aehlig et al. [Aehlig et al. 2005] independently. The former group introduced panic automata and proved its equi-expressivity with the class of recursion schemes; the latter introduced an essentially equivalent automata model called pushdown automata with links.

In 2006, Ong established the MSO decidability of trees generated by recursion schemes of all finite orders [Ong 2006a]: he proved that the problem is  $n$ -EXPTIME complete. The result was obtained using techniques from innocent game semantics [Hyland and Ong 2000]; it does not rely on an equivalent automata model for generating trees.

A different, automata-theoretic, proof of Ong’s decidability result was subsequently obtained by Hague, Murawski, Ong and Serre [Hague et al. 2008]. Thanks to the equi-expressivity between recursion schemes and CPDA, and the well-known connections between MSO model checking for trees and parity games, they show that the model-checking problem for recursion schemes is inter-reducible to the problem of determining the winner of a parity game played over the transition graph associated with a CPDA. Their work extends the techniques and results of (higher-order) pushdown games, including those of Walukiewicz [Walukiewicz 2001], Cachat [Cachat 2003] (see also [Carayol et al. 2008] for a comprehensive study on higher-order pushdown games) and Knapik et al. [Knapik et al. 2005]. These techniques have since been extended by Broadbent, Carayol, Ong and Serre to establish the closure of recursion schemes under MSO markings [Broadbent et al. 2010], and more recently by Carayol and Serre to prove that recursion schemes enjoy the effective MSO selection property [Carayol and Serre 2012].

Following initial ideas in [Aehlig 2006] and [Kobayashi 2009b], Kobayashi and Ong gave yet another proof of Ong’s decidability result. Their proof [Kobayashi and Ong 2009] consists in showing that, given a recursion scheme and an MSO formula or an equivalent property, one can construct an intersection type system such that the scheme is typable in the type system if and only if the property is satisfied by the scheme. Typability is then reduced to solving a parity game.

In [Salvati and Walukiewicz 2011; Salvati and Walukiewicz 2014], Salvati and Walukiewicz used Krivine machines [Krivine 2007] to represent the rewrite sequences of terms of the  $\lambda Y$ -calculus, a formalism equivalent to higher-order recursion schemes. A Krivine machine computes the weak head normal form of a  $\lambda Y$ -term using explicit substitutions. The MSO decidability for recursion schemes was then obtained by solving parity games played over the configurations of a Krivine machine. In [Salvati and Walukiewicz 2012; Salvati and Walukiewicz 2015] they also provide a translation from recursion schemes to CPDA which is very close to the translation independently obtained by Carayol and Serre in [Carayol and Serre 2012]. Also note that in both of these translations the authors remark that if the original recursion scheme is safe the CPDA that is obtained can safely be transformed into a higher-order pushdown automaton (i.e. all collapse operations can be replaced by a standard popping); this was actually previously established by Blum in [Blum 2017] and by Broadbent in his PhD thesis [Broadbent 2011, chapter 3] for the translation we provide in this paper (as given in its conference version [Hague et al. 2008]). Also remark that neither [Carayol and Serre 2012] nor [Salvati and Walukiewicz 2015] provide the translation back from CPDA to schemes.

Let us stress that even if the proof of the translation from schemes to CPDA we give here is longer than the ones in [Carayol and Serre 2012; Salvati and Walukiewicz 2015], it makes use of a richer higher-level concept (namely traversals) which we believe is worth knowing as it gives a deep insight on how scheme evaluation can be understood.

Structure of this paper

In this paper we present in full a proof of the equi-expressivity result which was first sketched in [Hague et al. 2008]. Owing to the length of this presentation, full proofs of the results therein on games played on the transition graphs of CPDA will be presented elsewhere.

The paper is organised as follows. Sections 2 and 3 introduce the main concepts, recursion schemes and CPDA respectively, together with examples. In Section 4 we state our main result. Then in Section 5 we give a transformation from CPDA to recursion schemes. The key idea is to associate a finite ground term with a given configuration of a CPDA and to

provide rewriting rules for those terms that can simulate transitions of the CPDA. This gives rise to a transition system over finite ground terms that is isomorphic to the transition graph of the CPDA. The final step consists in simulating this transition system by an appropriate recursion scheme. Finally Section 6 gives the transformation in the other direction. For this we consider an intermediate object, the traversal tree of the recursion scheme, which turns out to be equivalent to the tree generated by the scheme. We then use the traversal tree to design an equivalent CPDA that computes paths in the traversal tree.

## 2. RECURSION SCHEMES

### 2.1. Types and terms

Types are generated by the grammar  $A ::= o \mid A \rightarrow A$ . Every type  $A \neq o$  can be written uniquely as  $A_1 \rightarrow (A_2 \rightarrow \cdots \rightarrow (A_n \rightarrow o) \cdots)$ , for some  $n \geq 1$  which is called its arity; the ground type  $o$  has arity 0. We follow the convention that arrows associate to the right, and simply write  $A_1 \rightarrow A_2 \rightarrow \cdots \rightarrow A_n \rightarrow o$ , which we sometimes abbreviate to  $(A_1, \dots, A_n, o)$ . The order of a type measures the nesting depth on the left of  $\rightarrow$ . We define  $ord(o) = 0$  and  $ord(A_1 \rightarrow A_2) = \max(ord(A_1) + 1, ord(A_2))$ . Thus  $ord(A_1 \rightarrow \dots \rightarrow A_n \rightarrow o) = 1 + \max\{ord(A_i) \mid 1 \leq i \leq n\}$ . For example,  $ord(o \rightarrow o \rightarrow o \rightarrow o) = 1$  and  $ord(((o \rightarrow o) \rightarrow o) \rightarrow o) = 3$ .

Let  $\Sigma$  be a ranked alphabet i.e. each  $\Sigma$ -symbol  $f$  has an arity  $ar(f) \geq 0$  which determines its type  $\underbrace{o \rightarrow \cdots \rightarrow o}_{ar(f)} \rightarrow o$ . Further we assume that each symbol  $f \in \Sigma$  is assigned a finite

set  $Dir(f) = \{1, \dots, ar(f)\}$  of directions, and we define  $Dir(\Sigma) = \bigcup_{f \in \Sigma} Dir(f)$ . Let  $D$  be a set of directions; a  $D$ -tree is just a prefix-closed subset of  $D^*$ , the free monoid of  $D$ . A  $\Sigma$ -labelled ranked and ordered tree (or simply a  $\Sigma$ -labelled tree) is a function  $t : Dom(t) \rightarrow \Sigma$  such that  $Dom(t)$  is a  $Dir(\Sigma)$ -tree, and for every node  $\alpha \in Dom(t)$ , the  $\Sigma$ -symbol  $t(\alpha)$  has arity  $k$  if and only if  $\alpha$  has exactly  $k$  children and the set of its children is  $\{\alpha 1, \dots, \alpha k\}$ . We write  $\mathcal{T}^\infty(\Sigma)$  for the set of (finite and infinite)  $\Sigma$ -labelled trees.

Let  $\Xi$  be a set of typed symbols. Let  $f \in \Xi$  and  $A$  be a type, we write  $f : A$  to mean that  $f$  has type  $A$ . The set of (applicative) terms of type  $A$  generated from  $\Xi$ , written  $\mathcal{T}_A(\Xi)$ , is defined by induction over the following rules. If  $f : A$  is an element of  $\Xi$  then  $f \in \mathcal{T}_A(\Xi)$ ; if  $s \in \mathcal{T}_{A \rightarrow B}(\Xi)$  and  $t \in \mathcal{T}_A(\Xi)$  then  $s t \in \mathcal{T}_B(\Xi)$ . For simplicity we write  $\mathcal{T}(\Xi)$  to mean  $\mathcal{T}_o(\Xi)$ , the set of terms of ground type. Let  $t$  be a term, we write  $t : A$  to mean that  $t$  is a term of type  $A$ . In case  $\Xi$  is a ranked alphabet (and so every  $\Xi$ -symbol has an order-0 or order-1 type as determined by its arity) we identify terms in  $\mathcal{T}(\Xi)$  with the finite trees in  $\mathcal{T}^\infty(\Xi)$ .

### 2.2. Recursion schemes

For each type  $A$ , we assume an infinite set  $Var_A$  of variables of type  $A$ , such that  $Var_A$  and  $Var_B$  are disjoint whenever  $A \neq B$ ; and we write  $Var$  for the union of  $Var_A$  as  $A$  ranges over types. We use letters  $x, y, \varphi, \psi, \chi, \xi$  etc. to range over variables.

A (deterministic) recursion scheme is a quadruple  $G = \langle \Sigma, \mathcal{N}, \mathcal{R}, S \rangle$  where

- $\Sigma$  is a ranked alphabet of terminals (including a distinguished symbol  $\perp : o$ )
- $\mathcal{N}$  is a finite set of typed non-terminals; we use upper-case letters  $F, H$ , etc. to range over non-terminals
- $S \in \mathcal{N}$  is a distinguished start symbol of type  $o$
- $\mathcal{R}$  is a finite set of rewrite rules, one for each non-terminal  $F : (A_1, \dots, A_n, o)$ , of the form

$$F \xi_1 \cdots \xi_n \rightarrow e$$

where each  $\xi_i$  is a variable of type  $A_i$ , and  $e$  is a term in  $\mathcal{T}(\Sigma \cup \mathcal{N} \cup \{\xi_1, \dots, \xi_n\})$ . Note that the expressions on either side of the arrow are terms of ground type.

The order of a recursion scheme is defined to be the highest order of (the types of) its non-terminals.

In this paper we use recursion schemes as generators of  $\Sigma$ -labelled trees. Informally the value tree<sup>2</sup>  $\llbracket G \rrbracket$  of (or the tree generated by) a recursion scheme  $G$  is a possibly infinite term (of ground type), constructed from the terminals in  $\Sigma$ , that is obtained, starting from the start symbol  $S$ , by unfolding the rewrite rules of  $G$  ad infinitum, replacing formal by actual parameters each time.

To define  $\llbracket G \rrbracket$ , we first introduce a map  $(\cdot)^\perp : \bigcup_A \mathcal{T}_A(\Sigma \cup \mathcal{N}) \rightarrow \bigcup_{A: \text{ord}(A) \leq 1} \mathcal{T}_A(\Sigma)$  that takes a term and replaces each non-terminal, together with its arguments, by  $\perp$ . We define  $(\cdot)^\perp$  by structural recursion as follows: we let  $f$  range over  $\Sigma$ -symbols, and  $F$  over non-terminals in  $\mathcal{N}$

$$\begin{aligned} f^\perp &= f \\ F^\perp &= \perp \\ (st)^\perp &= \begin{cases} \perp & \text{if } s^\perp = \perp \\ (s^\perp t^\perp) & \text{otherwise.} \end{cases} \end{aligned}$$

Clearly if  $s \in \mathcal{T}(\Sigma \cup \mathcal{N})$  is of ground type, so is  $s^\perp \in \mathcal{T}(\Sigma)$ .

Next we define a one-step reduction relation  $\rightarrow_G$  which is a binary relation over terms in  $\mathcal{T}(\Sigma \cup \mathcal{N})$ . Informally,  $s \rightarrow_G s'$  just if  $s'$  is obtained from  $s$  by replacing some occurrence of a non-terminal  $F$  by the right-hand side of its rewrite rule in which all formal parameters are in turn replaced by their respective actual parameters, subject to the proviso that the  $F$  must occur at the head of a subterm of ground type. Formally  $\rightarrow_G$  is defined by induction over the following rules:

- (Substitution).  $Ft_1 \cdots t_n \rightarrow_G e[t_1/\xi_1, \dots, t_n/\xi_n]$  where  $F\xi_1 \cdots \xi_n \rightarrow e$  is a rewrite rule of  $G$ .
- (Context). If  $t \rightarrow_G t'$  then  $(st) \rightarrow_G (st')$  and  $(ts) \rightarrow_G (t's)$ .

Note that  $\mathcal{T}^\infty(\Sigma)$  is a complete partial order with respect to the approximation ordering  $\sqsubseteq$  defined by:  $t \sqsubseteq t'$  just if  $\text{Dom}(t) \subseteq \text{Dom}(t')$  and for all  $w \in \text{Dom}(t)$ , we have  $t(w) = \perp$  or  $t(w) = t'(w)$ . I.e.  $t'$  is obtained from  $t$  by replacing some  $\perp$ -labelled nodes by  $\Sigma$ -labelled trees. If one views  $G$  as a rewrite system, it is a consequence of the Church-Rosser property [Church and Rosser 1936] that the set  $\{t^\perp \in \mathcal{T}^\infty(\Sigma) : \text{there is a finite reduction sequence } S = t_0 \rightarrow_G \cdots \rightarrow_G t_n = t\}$  is directed. Hence, we can finally define the  $\Sigma$ -labelled ranked tree  $\llbracket G \rrbracket$ , called the value tree of (or the tree generated by)  $G$ :

$$\llbracket G \rrbracket = \sup\{t^\perp \in \mathcal{T}^\infty(\Sigma) : \text{there is a finite reduction sequence } S = t_0 \rightarrow_G \cdots \rightarrow_G t_n = t\}.$$

We write  $\mathbf{RecTree}_n \Sigma$  for the class of value trees  $\llbracket G \rrbracket$  where  $G$  ranges over order- $n$  recursion schemes.

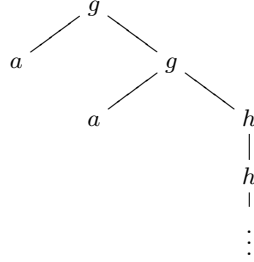
**Example 2.1.** Let  $G_1$  be the order-2 recursion scheme with non-terminals  $\{S : o, H : (o, o), F : ((o, o), o)\}$ , variables  $\{z : o, \varphi : (o, o)\}$ , terminals  $g, h, a$  of arity 2, 1, 0 respectively,

<sup>2</sup>We refer to the  $\Sigma$ -labelled tree generated by a recursion scheme as its value tree, because the name is a good counterpoint to computation tree. We have in mind here the distinction between value and computation emphasized by Moggi [Moggi 1989]. The idea is that the value tree is obtained from the computation tree by a (possibly infinite) process of evaluation.

and the following rewrite rules:

$$\begin{aligned} S &\rightarrow H a \\ H z &\rightarrow F (g z) \\ F \varphi &\rightarrow \varphi(\varphi(F h)) \end{aligned}$$

The value tree  $\llbracket G \rrbracket$  is the  $\Sigma$ -labelled tree representing the infinite term  $g a (g a (h (h (h \dots))))$ :

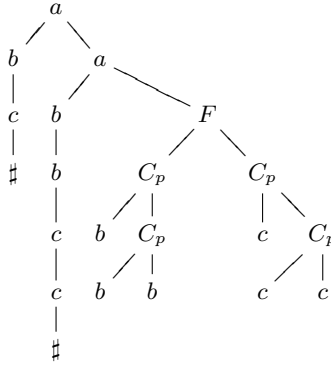


The only infinite path in the tree is the node-sequence  $\varepsilon \cdot 2 \cdot 22 \cdot 221 \cdot 2211 \dots$ .

Example 2.2. Let  $G_2$  be the order-2 recursion scheme with non-terminals  $\{S : o, F : ((o, o), (o, o), o), C_p : ((o, o), (o, o), o, o)\}$ , variables  $\{x : o, \varphi : (o, o), \psi : (o, o)\}$ , terminals  $a, b, c, \#$  of arity 2, 1, 1, 0 respectively, and the following rewrite rules:

$$\begin{aligned} S &\rightarrow F b c \\ F \varphi \psi &\rightarrow a(\varphi(\psi \#))(F(C_p b \varphi)(C_p c \psi)) \\ C_p \varphi \psi x &\rightarrow \varphi(\psi x) \end{aligned}$$

After some applications of the rules, one gets the following term:



The value tree  $\llbracket G \rrbracket$  is the  $\Sigma$ -labelled tree representing the infinite term

$$a(bc\#)(a(bbcc\#)(a(bbbccc\#)\dots))$$

In particular, the path language of  $t$  (i.e. the set of words obtained by considering the labels along a maximal branch) is  $\{a^\omega\} \cup \{a^k b^k c^k \# \mid k \geq 1\}$ .

### 2.3. The safety constraint

The safety constraint on applicative terms may be regarded as a reformulation of Damm's derived types [Damm 1982]. To define safety, we first introduce homogeneous types. The type



$(A_1, \dots, A_n, o)$  is homogeneous just if each  $A_i$  is homogeneous, and  $ord(A_1) \geq ord(A_2) \geq \dots \geq ord(A_n)$ . It follows that the ground type  $o$  and all order-1 types are homogeneous. In the following definition, suppose a term  $s$  has type  $A$ , then we write  $ord(s) = ord(A)$ .

**Definition 2.3.** A rewrite rule  $F x_1 \dots x_n \rightarrow t$  is safe just if

- (i) the type of  $F$  and of all subterms of  $t$  are homogeneous, and
- (ii) for each subterm  $s$  of  $t$  that occurs in the operand position of an application, and for each  $1 \leq i \leq n$ , if  $x_i$  occurs in  $s$  then  $ord(s) \leq ord(x_i)$ .

We say that a recursion scheme is safe just if all its rewrite rules are safe.

It follows from the definition that all recursion schemes of order at most 1 are safe. For a study of safety in the setting of the simply-typed lambda calculus, see [Blum and Ong 2009].

**Example 2.4.** The scheme  $G_1$  defined in Example 2.1 is unsafe because of the second rule. The subterm  $gz$  occurs at an operand position and has order 1, but  $z$  has order 0.

Paweł Urzyczyn conjectured that safety is a genuine constraint on expressivity i.e. there is a tree, generated by an order-2 unsafe scheme, which cannot be generated by any safe recursion scheme of any order. This conjecture was recently proved by Paweł Parys [Parys 2011; Parys 2012].

### 3. COLLAPSIBLE PUSHDOWN AUTOMATA (CPDA)

We introduce (higher-order) collapsible pushdown automata (CPDA). An order- $n$  CPDA, or  $n$ -CPDA for short, is just an order- $n$  pushdown automaton ( $n$ -PDA), in the sense of [Knapik et al. 2002], in which every non- $\perp$  symbol in the order- $n$  stack has a link to a (necessarily lower-ordered) stack situated below it. In the following section we give an exposition where links are treated informally. A more formal treatment of the links is given in Section 3.2.

#### 3.1. Stacks with links

Fix a stack alphabet  $\Gamma$  and a distinguished bottom-of-stack symbol  $\perp \in \Gamma$ . An order- $\mathbf{0}$  stack (or simply  $\mathbf{0}$ -stack) is just a stack symbol. An order- $(\mathbf{n} + \mathbf{1})$  stack (or simply  $(\mathbf{n} + \mathbf{1})$ -stack)  $s$  is a non-null sequence (written  $[s_1 \dots s_l]$ ) of  $n$ -stacks such that every non- $\perp$   $\Gamma$ -symbol  $\gamma$  that occurs in  $s$  has a link to a stack of some order  $e$  (say, where  $0 \leq e \leq n$ ) situated below it in  $s$ ; we call the link an  $(\mathbf{e} + \mathbf{1})$ -link. The order of a stack  $s$  is written  $ord(s)$ .

As usual, the bottom-of-stack symbol  $\perp$  cannot be popped from or pushed onto a stack. Thus we require an order-1 stack to be a non-null sequence  $[\gamma_1 \dots \gamma_l]$  of elements of  $\Gamma$  such that for all  $1 \leq i \leq l$ ,  $\gamma_i = \perp$  iff  $i = 1$ . We define  $\perp_k$ , the empty  $\mathbf{k}$ -stack, as follows:  $\perp_0 = \perp$  and  $\perp_{k+1} = [\perp_k]$ .

We first define the operations  $pop_i$  and  $top_i$  with  $i \geq 1$ :  $top_i(s)$  returns the top  $(i-1)$ -stack of  $s$ , and  $pop_i(s)$  returns  $s$  with its top  $(i-1)$ -stack removed. Precisely let  $s = [s_1 \dots s_{l+1}]$  be a stack with  $1 \leq i \leq ord(s)$ :

$$\begin{aligned} top_i(\underbrace{[s_1 \dots s_{l+1}]}_s) &= \begin{cases} s_{l+1} & \text{if } i = ord(s) \\ top_i(s_{l+1}) & \text{if } i < ord(s) \end{cases} \\ pop_i(\underbrace{[s_1 \dots s_{l+1}]}_s) &= \begin{cases} [s_1 \dots s_l] & \text{if } i = ord(s) \text{ and } l \geq 1 \\ [s_1 \dots s_l pop_i(s_{l+1})] & \text{if } i < ord(s) \end{cases} \end{aligned}$$



By abuse of notation, we set  $top_{ord(s)+1}(s) = s$ . Note that  $pop_i(s)$  is undefined if  $top_{i+1}(s)$  is a one-element  $i$ -stack. For example  $pop_2([[ \perp \alpha \beta ]])$  and  $pop_1([[ \perp \alpha \beta ] [ \perp ]])$  are both undefined.

There are two kinds of *push* operations. We start with the order-1 *push*. Let  $\gamma$  be a non- $\perp$  stack symbol and  $1 \leq e \leq ord(s)$ , we define a new stack operation  $push_1^{\gamma,e}$  that, when applied to  $s$ , first attaches a link from  $\gamma$  to the  $(e-1)$ -stack immediately below the top  $(e-1)$ -stack of  $s$ , then pushes  $\gamma$  (with its link) onto the top 1-stack of  $s$ . Formally for  $1 \leq e \leq ord(s)$  and  $\gamma \in (\Gamma \setminus \{ \perp \})$ , we define

$$push_1^{\gamma,e}(\underbrace{[s_1 \cdots s_l]_s}_{s_{l+1}}) = \begin{cases} [s_1 \cdots s_l push_1^{\gamma,e}(s_{l+1})] & \text{if } e < ord(s) \\ [s_1 \cdots s_l s_{l+1} \gamma^\dagger] & \text{if } e = ord(s) = 1 \\ [s_1 \cdots s_l push_1^{\hat{\gamma}}(s_{l+1})] & \text{if } e = ord(s) \geq 2 \text{ and } l \geq 1 \end{cases}$$

where

- $\gamma^\dagger$  denotes the symbol  $\gamma$  with a link to the 0-stack  $s_{l+1}$
- $\hat{\gamma}$  denotes the symbol  $\gamma$  with a link to the  $(e-1)$ -stack  $s_l$ ; and we define

$$push_1^{\hat{\gamma}}(\underbrace{[t_1 \cdots t_r]_t}_{t_{r+1}}) = \begin{cases} [t_1 \cdots t_r push_1^{\hat{\gamma}}(t_{r+1})] & \text{if } ord(t) > 1 \\ [t_1 \cdots t_{r+1} \hat{\gamma}] & \text{otherwise i.e. } ord(t) = 1 \end{cases}$$

The higher-order  $push_j$ , where  $j \geq 2$ , simply duplicates the top  $(j-1)$ -stack of  $s$ , including all the links. Precisely, let  $s = [s_1 \cdots s_{l+1}]$  be a stack with  $2 \leq j \leq ord(s)$ :

$$push_j(\underbrace{[s_1 \cdots s_{l+1}]_s}_{s_{l+1}}) = \begin{cases} [s_1 \cdots s_{l+1} s_{l+1}] & \text{if } j = ord(s) \\ [s_1 \cdots s_l push_j(s_{l+1})] & \text{if } j < ord(s) \end{cases}$$

Note that in case  $j = ord(s)$  above, the link structure of  $s_{l+1}$  is preserved by the copy that is pushed on top by  $push_j$ .

Finally there is an important operation called *collapse*. We say that the  $n$ -stack  $s_0$  is a prefix of an  $n$ -stack  $s$ , written  $s_0 \leq s$ , just in case  $s_0$  can be obtained from  $s$  by a sequence of (possibly higher-order) *pop* operations. Take an  $n$ -stack  $s$  where  $s_0 \leq s$ , for some  $n$ -stack  $s_0$ , and  $top_1 s$  has a link to  $top_e(s_0)$ . Then *collapse*  $s$  is defined to be  $s_0$ .

Example 3.1. When displaying  $n$ -stacks in examples, we use bent arrows to denote links; however to avoid clutter we shall omit 1-links (indeed by construction they can only point to the symbol directly below), writing e.g.  $[[ \perp ] [ \perp \alpha \beta ]]$  instead of  $[[ \perp ] [ \perp \overset{\curvearrowright}{\alpha} \overset{\curvearrowright}{\beta} ]]$ .

Take the 3-stack  $s = [[ [ \perp \alpha ] ] [ [ \perp ] [ \perp \alpha ] ]]$ . We have

$$\begin{aligned} push_1^{\beta,2}(s) &= [[ [ \perp \alpha ] ] [ [ \perp ] [ \perp \alpha \beta ] ]] \\ collapse(push_1^{\beta,2}(s)) &= [[ [ \perp \alpha ] ] [ [ \perp ] ]] \\ \underbrace{push_1^{\gamma,3}(push_1^{\beta,2}(s))}_\theta &= [[ [ \perp \alpha ] ] [ [ \perp ] [ \perp \alpha \beta \gamma ] ]]. \end{aligned}$$

Then  $push_2(\theta)$  and  $push_3(\theta)$  are respectively

$$\begin{array}{c}
\begin{array}{c} \curvearrowright \\ \curvearrowright \\ \curvearrowright \end{array} \\
[[[\perp \alpha]] \quad [[[\perp]] [\perp \alpha \beta \gamma] \quad [\perp \alpha \beta \gamma]]] \text{ and} \\
\begin{array}{c} \curvearrowleft \\ \curvearrowleft \\ \curvearrowleft \end{array} \\
[[[\perp \alpha]] \quad [[[\perp]] [\perp \alpha \beta \gamma]] \quad [[[\perp]] [\perp \alpha \beta \gamma]]].
\end{array}$$

We have  $\text{collapse}(\text{push}_2(\theta)) = \text{collapse}(\text{push}_3(\theta)) = \text{collapse}(\theta) = [[[\perp \alpha]]]$ .

### 3.2. A formal definition of CPDA stack operations

One way to give a formal semantics of the stack operations is to work with appropriate numeric representations of the links. In [Knapik et al. 2005], it has been shown how this can be done in the order-2 case in the setting of panic automata. Here we use a different encoding of stacks with links that works for all orders. The presentation follows Kartzow [Kartzow 2010].

The idea is simple: take an order- $n$  stack  $s$  and suppose that there is a link from (a particular occurrence of) a symbol  $\gamma$  in  $s$  to some  $(e-1)$ -stack  $s'$ , and that  $s'$  is the  $k$ -th element of the  $e$ -stack that contains it. In the formal definition, a symbol-with-link of an order- $n$  CPDA is written  $\gamma^{(e,k)}$ , where  $\gamma \in \Gamma$ ,  $1 \leq e \leq n$  and  $k \geq 1$ . Purely for convenience, we require that if  $\gamma = \perp$  then  $e = 1$  and  $k = 0$ .

The set  $Op_n$  of order- $n$  CPDA stack operations comprises four types of operations:

- (1)  $pop_k$  for each  $1 \leq k \leq n$
- (2)  $push_j$  for each  $2 \leq j \leq n$
- (3)  $push_1^{\gamma,e}$  for each  $1 \leq e \leq n$  and each  $\gamma \in (\Gamma \setminus \{\perp\})$ , and
- (4)  $collapse$ .

We begin by defining an operation that truncates a stack.

$$\text{bot}_i^k(\underbrace{[t_1 \cdots t_{r+1}]}_t) = \begin{cases} [t_1 \cdots t_k] & \text{if } \text{ord}(t) = i \text{ and } k \leq r \\ [t_1 \cdots t_r \text{ bot}_i^k(t_{r+1})] & \text{if } \text{ord}(t) < i \text{ and } k \leq r \end{cases}$$

We can now define our stack operations. Let  $1 \leq e \leq \text{ord}(s)$ . We first define  $push_1^{\gamma,e}$

We first define  $push_1^\gamma$  to aid in the definition of  $push_1^{\gamma,e}$ .

$$\text{push}_1^\gamma(\underbrace{[t_1 \cdots t_{r+1}]}_t) = \begin{cases} [t_1 \cdots t_r \text{ push}_1^\gamma(t_{r+1})] & \text{if } \text{ord}(t) > 1 \\ [t_1 \cdots t_{r+1} \gamma] & \text{otherwise i.e. } \text{ord}(t) = 1 \end{cases}$$

Then we have

$$\text{push}_1^{\gamma,e}(t) = \text{push}_1^{\gamma^{(e,k)}}(t)$$

assuming  $\text{top}_{e+1}(t) = [s_1 \cdots s_{k+1}]$  if  $e > 1$ , and  $\text{top}_2(t) = [s_1 \cdots s_k]$  for  $e = 1$ . We are now ready to define the  $collapse$  operation by letting

$$\text{collapse}(s) = \text{bot}_e^k(s) \quad \text{where } \text{top}_1(s) = \gamma^{(e,k)} \text{ and } k > 0$$

One can think of the  $collapse$  operation as a generalisation of the  $pop_k$  operation for any  $k > 1$  as we have for any stack  $s$  and any  $k > 1$  that  $pop_k(s) = \text{collapse}(\text{push}_1^{\gamma,k}(s))$  for an arbitrary dummy symbol  $\gamma$ .

Now for  $2 \leq j \leq \text{ord}(s)$ :

$$\text{push}_j(\underbrace{[s_1 \cdots s_{l+1}]_s}) = \begin{cases} [s_1 \cdots s_{l+1} s_{l+1}] & \text{if } j = \text{ord}(s) \\ [s_1 \cdots s_l \text{push}_j(s_{l+1})] & \text{if } j < \text{ord}(s) . \end{cases}$$

. Note that, as an easy consequence of the definitions of the  $\text{push}_1^{\gamma,e}$  and the  $\text{push}_k$  operations, a link of order  $e$  always points to a  $(e-1)$ -stack inside the current  $e$ -stack.

Example 3.2. Let us now revisit Example 3.1. Take the 3-stack  $s = [[[\perp \alpha]] [\perp \alpha]]$ . (To save writing, we omit the superscripts of the form  $(1, k)$ .) We have

$$\begin{aligned} \text{push}_1^{\beta,2}(s) &= [[[\perp \alpha]] [[\perp] [\perp \alpha \beta^{(2,1)}]]] \\ \text{push}_1^{\gamma,3}(\text{push}_1^{\beta,2}(s)) &= [[[\perp \alpha]] [[\perp] [\perp \alpha \beta^{(2,1)} \gamma^{(3,1)}]]] \\ \text{push}_2(\text{push}_1^{\gamma,3}(\text{push}_1^{\beta,2}(s))) &= [[[\perp \alpha]] [[\perp] [\perp \alpha \beta^{(2,1)} \gamma^{(3,1)}] [\perp \alpha \beta^{(2,1)} \gamma^{(3,1)}]]] \\ \text{push}_3(\text{push}_1^{\gamma,3}(\text{push}_1^{\beta,2}(s))) &= [[[\perp \alpha]] [[\perp] [\perp \alpha \beta^{(2,1)} \gamma^{(3,1)}] [[\perp] [\perp \alpha \beta^{(2,1)} \gamma^{(3,1)}]]] \end{aligned}$$

and we have

$$\text{collapse}(\text{push}_2(\text{push}_1^{\gamma,3}(\text{push}_1^{\beta,2}(s)))) = \text{collapse}(\text{push}_3(\text{push}_1^{\gamma,3}(\text{push}_1^{\beta,2}(s)))) = [[[\perp \alpha]]]$$

Note that in the sequel we will use the informal presentation of stacks with links rather than the formal one.

### 3.3. Tree-generating CPDA

Collapsible pushdown automata are a generalization (to all finite orders) of pushdown automata with links [Aehlig et al. 2004; Aehlig et al. 2005], which are essentially the same as panic automata [Knapik et al. 2005].

We define collapsible pushdown automata (CPDA) as automata with a finite control and a stack with links as memory.

Definition 3.3. An order- $n$  (deterministic) collapsible pushdown automaton ( $n$ -CPDA) is a 5-tuple  $\mathcal{A} = \langle A \cup \{\varepsilon\}, \Gamma, Q, \delta, q_I \rangle$  where  $A$  is an input alphabet and  $\varepsilon$  is a special symbol,  $\Gamma$  is a stack alphabet,  $Q$  is a finite set of control states,  $q_I \in Q$  is the initial state, and  $\delta : Q \times \Gamma \times (A \cup \{\varepsilon\}) \rightarrow Q \times \text{Op}_n$  is a transition (partial) function such that, for all  $q \in Q$  and  $\gamma \in \Gamma$ , if  $\delta(q, \gamma, \varepsilon)$  is defined then for all  $a \in A$ ,  $\delta(q, \gamma, a)$  is undefined i.e. if an  $\varepsilon$ -transition can be taken, then no other transitions are possible.

As CPDA will be used to generate ranked tree (as explained below),  $A$  will always be here of the form  $\{1, \dots, d\}$  for some integer  $d$ .

In the special case where  $\delta(q, \gamma, \varepsilon)$  is undefined for all  $q \in Q$  and  $\gamma \in \Gamma$  we refer to  $\mathcal{A}$  as an  $\varepsilon$ -free  $n$ -CPDA.

Configurations of an  $n$ -CPDA are pairs of the form  $(q, s)$  where  $q \in Q$  and  $s$  is an  $n$ -stack with links over  $\Gamma$ ; we call  $(q_I, \perp_n)$  the initial configuration.

An  $n$ -CPDA  $\mathcal{A} = \langle A \cup \{\varepsilon\}, \Gamma, Q, \delta, q_I \rangle$  naturally defines an  $(A \cup \{\varepsilon\})$ -labelled transition graph  $\text{Graph}(\mathcal{A}) := (V, E \subseteq V \times (A \cup \{\varepsilon\}) \times V)$  whose vertices  $V$  are the configurations of  $\mathcal{A}$  and whose edge relation  $E$  is given by:  $((q, s), a, (q', s')) \in E$  iff  $\delta(q, \text{top}_1(s), a) = (q', \text{op})$  and  $s' = \text{op}(s)$ . Such a graph is called an  $n$ -CPDA graph. We shall use the notation  $v \xrightarrow{a} v'$  to mean that  $(v, a, v') \in E$ , and  $v \xrightarrow{a_1 a_2 \cdots a_\ell} v'$  to mean that there exist  $v_0, \dots, v_\ell \in V$  such that  $v_0 = v$ ,  $v_\ell = v'$  and  $v_i \xrightarrow{a_{i+1}} v_{i+1}$  for all  $0 \leq i < \ell$ .

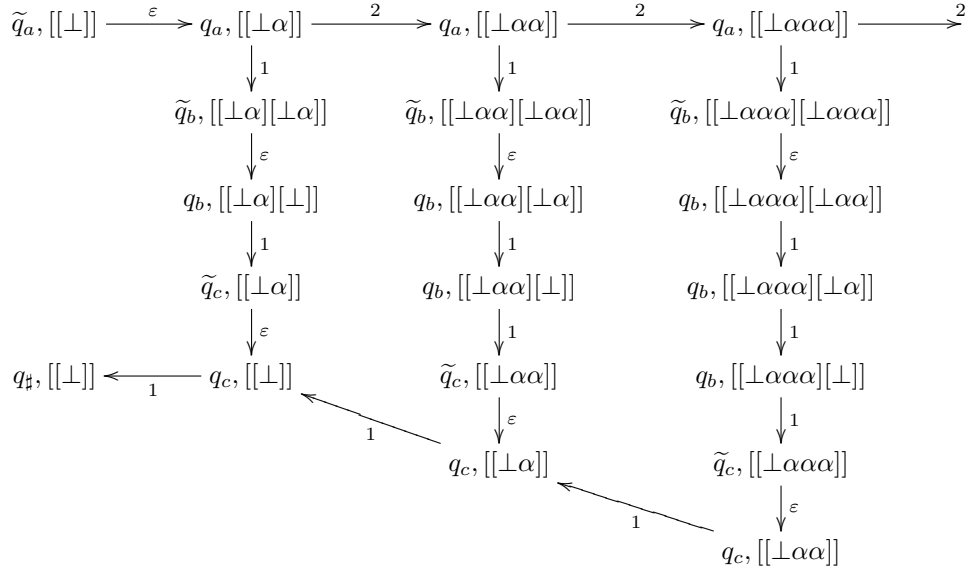


Fig. 1. Transition graph of the CPDA of Example 3.4

Note that one can transform  $\mathcal{A}$ , while preserving its transition graph, so that in every configuration  $(q, s)$  reachable from the initial one, whenever  $\delta(q, \text{top}_1 s, a) = (q', \text{op})$  is defined, so is  $\text{op}(s)$  i.e. whenever a transition is possible, the corresponding stack action is well-defined. Such a transformation can be obtained by storing in the stack extra information about feasibility of the  $\text{pop}_k$  operation<sup>3</sup>. In the following we always assume that we are in such a setting.

Example 3.4. Consider the following 2-CPDA (that actually does not make use of links)  $\mathcal{A} = \langle \{1, 2, \varepsilon\}, \{\perp, \alpha\}, \{q_a, q_b, q_c, q_\#, \tilde{q}_a, \tilde{q}_b, \tilde{q}_c\}, \delta, \tilde{q}_a \rangle$  with  $\delta$  as follows (we only give those transitions that may happen):

- $\delta(\tilde{q}_a, \perp, \varepsilon) = \delta(q_a, \alpha, 2) = (q_a, \text{push}_1^\alpha)$ ;
- $\delta(q_a, \alpha, 1) = (\tilde{q}_b, \text{push}_2)$ ;
- $\delta(\tilde{q}_b, \alpha, \varepsilon) = \delta(q_b, \alpha, 1) = (q_b, \text{pop}_1)$ ;
- $\delta(q_b, \perp, 1) = (\tilde{q}_c, \text{pop}_2)$ ;
- $\delta(\tilde{q}_c, \alpha, \varepsilon) = \delta(q_c, \alpha, 1) = (q_c, \text{pop}_1)$ ;
- $\delta(q_c, \perp, 1) = (q_\#, \text{id})$  where  $\text{id}$  is the operation that leaves the stack unchanged;
- $\delta(q_\#, \perp, \_)$  is undefined.

Then  $\text{Graph}(\mathcal{A})$  is given in Figure 1.

We now explain how to define from  $\mathcal{A}$  a  $(\Sigma \cup \{\perp\})$ -labelled ranked tree  $t$  for a ranked alphabet  $\Sigma$  where  $\perp$  is an additional symbol of arity 0. The idea is first to unfold  $\text{Graph}(\mathcal{A})$ , then to contract the  $\varepsilon$ -transitions, and finally to label the nodes carefully.

A vertex  $v$  in  $\text{Graph}(\mathcal{A})$  is non-productive if it is the source of an infinite path labelled by  $\varepsilon^\omega$  i.e. for every  $k \geq 0$  there exists  $v_k$  such that  $v \xrightarrow{\varepsilon} v_1 \xrightarrow{\varepsilon} v_2 \xrightarrow{\varepsilon} v_3 \cdots$ . Otherwise  $v$  is said to be productive.

<sup>3</sup>This can be done by extending higher-order PDA to allow the annotation of each order- $k$  stack with the feasibility of the  $\text{pop}_k$  operation, which can in turn be transformed into a standard higher-order PDA following the remark on Page 9 of Knapik et al. [Knapik et al. 2002].

First we assume that  $A = \{1, \dots, d\}$  for some  $d \geq 1$ , and whenever  $\{a \in A \mid (q, \gamma, a) \in \text{Dom}(\delta)\}$  has  $k$  elements then it is  $\{1, \dots, k\}$ . And we consider a partial function  $\rho : Q \times \Gamma \rightarrow \Sigma$  such that for every  $q$  and  $\gamma$  if  $(q, \gamma, \varepsilon) \notin \text{Dom}(\delta)$  then  $(q, \gamma) \in \text{Dom}(\rho)$  and  $\{a \in A \mid (q, \gamma, a) \in \text{Dom}(\delta)\} = \text{Dir}(\rho(q, \gamma))$ ; We will use the function  $\rho$  to define the node labels of the tree  $t$  being constructed.

We set  $\text{Dom}(t)$  to be the prefix-closed subset of  $A^*$  defined by

$$\text{Dom}(t) := \{w \in A^* \mid \exists v \in V. (q_I, \perp_n) \xrightarrow{w} v\}.$$

Thanks to determinism, for all  $w \in \text{Dom}(t)$  there is a unique vertex  $v_w$  such that  $(q_I, \perp_n) \xrightarrow{w} v_w$  and such that  $v_w \xrightarrow{\varepsilon} v$  holds whenever  $(q_I, \perp_n) \xrightarrow{w} v$ .

In case  $v_w$  is productive, define  $(q_w, s_w)$  to be the unique configuration with  $v_w \xrightarrow{\varepsilon} (q_w, s_w)$  that is not the source of an  $\varepsilon$  transition, i.e. that is such that  $(q_w, \text{top}_1(s_w), \varepsilon) \notin \text{dom}(\delta)$ .

We can finally define

$$t(w) := \begin{cases} \perp & \text{if } v_w \text{ is non-productive;} \\ \rho(q_w, \text{top}_1(s_w)) & \text{otherwise.} \end{cases}$$

Hence there are two kinds of leaves in  $t$ : those labelled by symbols in  $\Sigma$  which correspond to dead-ends in  $\text{Graph}(\mathcal{A})$ , and those labelled by  $\perp$  which correspond to non-productive vertices in  $\text{Graph}(\mathcal{A})$ . Note the analogy with trees generated by recursion schemes, where  $\perp$  is used to label those leaves that correspond to an infinite sequence of “non-productive” rewritings.

**Example 3.5.** Consider the CPDA  $\mathcal{A}$  from Example 3.4 and let  $\rho(q_a, \_) = a$ ,  $\rho(q_b, \_) = b$ ,  $\rho(q_c, \_) = c$ ,  $\rho(q_\#, \_) = \#$ , where  $\_$  stands for any stack symbol. Then, the tree generated by  $\mathcal{A}$  and  $\rho$  is the same as the one generated by the order-2 recursion scheme of Example 2.2.

**Remark 3.6.** Thanks to  $\varepsilon$ -transitions, we can safely assume that the labelling function  $\rho$  only depends on the control state i.e.  $\rho : Q \rightarrow \Sigma$  instead of  $\rho : Q \times \Gamma \rightarrow \Sigma$ , as in Example 3.5. One can always encode the current top stack symbol in the control state: after each transition, perform an  $\varepsilon$ -transition that updates the control state according to the top stack symbol.

**Remark 3.7.** A natural variant of CPDA allows the execution of several stack operations per transition i.e. by defining  $\delta : Q \times \Gamma \times (A \cup \{\varepsilon\}) \rightarrow Q \times \text{Op}_n^*$ . To simulate such a variant by a standard CPDA, it suffices to add intermediate states to track a sequence of stack operations by a finite sequence of  $\varepsilon$ -transitions.

**Remark 3.8.** By allowing several stack operations per transition, one can get rid of the states by encoding them in the stack symbols. In this setting, given a CPDA without state (i.e. with a dummy single state) but allowing several stack operations per transition, a ranked tree can be generated by unfolding the transition graph and taking the  $\varepsilon$ -closure (i.e. we contract each  $\varepsilon$ -labelled edge, merging its source and target vertices). The nodes are labelled according to a function  $\rho : \Gamma \rightarrow \Sigma$ . It is easy to check that such a variant CPDA is equi-expressive with the standard CPDA for generating trees.

**Remark 3.9.** In [Knapik et al. 2002; Hague et al. 2008], deterministic higher-order pushdown automata and CPDA are used directly as tree-accepting device in a top-down fashion, allowing silent moves. When reading a node of an input tree in a given state, the automaton may make a number of  $\varepsilon$ -transitions (hence changing both the stack and the state) and then it branches by sending a copy of the automaton to read each child node in a state prescribed by the transition function. Thanks to the determinism, exactly one tree is accepted by the automaton. It is easy to see this definition coincides with our notion of tree generation by

an  $n$ -CPDA. Essentially branching corresponds to unfolding, and  $\varepsilon$ -transitions to taking the  $\varepsilon$ -closure of the unfolding.

#### 4. THE EQUI-EXPRESSIVITY THEOREM

In this paper we prove the following theorem.

Theorem 4.1 (Equi-Expressivity). Order- $n$  recursion schemes and  $n$ -CPDA are equi-expressive for generating trees. I.e. we have the following.

- (i) Let  $G$  be an order- $n$  recursion scheme over  $\Sigma$  and let  $t$  be its value tree. There is an order- $n$  CPDA  $\mathcal{A} = \langle A \cup \{\varepsilon\}, \Gamma, Q, \delta, q_0 \rangle$  and a function  $\rho : Q \rightarrow \Sigma$  such that  $t$  is the tree generated by  $\mathcal{A}$  and  $\rho$ .
- (ii) Let  $\mathcal{A} = \langle A \cup \{\varepsilon\}, \Gamma, Q, \delta, q_0 \rangle$  be an order- $n$  CPDA, and let  $t$  be the  $\Sigma$ -labelled tree generated by  $\mathcal{A}$  and a function  $\rho : Q \rightarrow \Sigma$ . There is an order- $n$  recursion scheme over  $\Sigma$  whose value tree is  $t$ .

Further the inter-translations between schemes and CPDA are polytime computable.

Theorem 4.1 extends to all recursion schemes the following result [Knapik et al. 2001] about safe recursion schemes. An  $n$ -PDA is just an  $n$ -CPDA that never performs a *collapse*.

Theorem 4.2 ([Knapik et al. 2001]). Order- $n$  safe recursion schemes and  $n$ -PDA are equi-expressive for generating trees. Moreover the inter-translations between safe schemes and  $n$ -PDA are polytime computable.

Theorem 4.1 also extends to all finite orders a similar result from [Knapik et al. 2005] restricted to order 2.

The rest of this paper is devoted to the proof of Theorem 4.1: Section 5 proves that schemes are at least as expressive as CPDA (Theorem 5.4) and Section 6 proves that CPDA are at least as expressive as schemes (Theorem 6.11).

#### 5. FROM CPDA TO RECURSION SCHEMES

For the rest of this section we fix an order- $n$  CPDA  $\mathcal{A} = \langle A \cup \{\varepsilon\}, \Gamma, Q, \delta, q_1 \rangle$  where  $Q = \{q_1, \dots, q_m\}$  and  $m \geq 1$ . We shall first introduce a representation of stacks and configurations of  $\mathcal{A}$  by terms which are then organised into a recursion scheme. Finally we show that the labelled transition system associated with the recursion scheme is identical to the labelled transition graph of  $\mathcal{A}$ .

##### 5.1. Term representation of stacks and configurations

We start by defining, for every  $0 \leq k \leq n$  a type denoted  $\mathbf{k}$  that will later be used to type the behaviour of a  $k$ -stack. First we identify the ground type  $o$  with a new type denoted  $\mathbf{n}$ . Inductively, for each  $0 \leq k < n$  we define a type

$$\mathbf{k} = (\mathbf{k} + \mathbf{1})^m \rightarrow (\mathbf{k} + \mathbf{1})$$

where, for types  $A$  and  $B$ , we write  $A^m \rightarrow B$  as a shorthand for  $\underbrace{A \rightarrow \dots \rightarrow A}_m \rightarrow B$ . In

particular, for every  $0 \leq k < n$ , we have

$$\mathbf{k} = (\mathbf{k} + \mathbf{1})^m \rightarrow (\mathbf{k} + \mathbf{2})^m \rightarrow \dots \rightarrow \mathbf{n}^m \rightarrow \mathbf{n}$$

We also introduce a non-terminal  $\text{Void}_k$  of type  $\mathbf{k}$  for each  $0 \leq k \leq n$ .

Assume  $s$  is an order- $n$  stack and  $p$  is a control state of  $\mathcal{A}$ . In the sequel, we will define, for every  $0 \leq k \leq n$ , a term  $\llbracket s \rrbracket_k^p : \mathbf{k}$  that represents the behaviour of the topmost  $k$ -stack in  $s$ , i.e.  $\text{top}_{k+1}(s)$ . To understand why  $\llbracket s \rrbracket_k^p$  is of type  $\mathbf{k}$  one can view an order- $k$  stack as acting on order- $(k+1)$  stacks: for every order- $(k+1)$  stack we can build a new order- $(k+1)$  stack

by pushing an order- $k$  stack on top of it. This behaviour has the type  $(\mathbf{k} + \mathbf{1}) \rightarrow (\mathbf{k} + \mathbf{1})$ . However, for technical reasons, when dealing with control states and configurations, we need to work with  $m$  copies of each stack, one for each control state. Hence we view a  $k$ -stack as mapping  $m$  copies of an order- $(k + 1)$  stack to a single order- $(k + 1)$  stack. This explains why  $\mathbf{k}$  is defined to be  $(\mathbf{k} + \mathbf{1})^m \rightarrow (\mathbf{k} + \mathbf{1})$ .

For every stack symbol  $\gamma$ , every  $1 \leq e \leq n$  and every state  $p \in Q$ , we introduce a non-terminal

$$\mathcal{F}_p^{\gamma,e} : \mathbf{e}^m \rightarrow \mathbf{1}^m \rightarrow \cdots \rightarrow \mathbf{n}^m \rightarrow \mathbf{n}$$

Note that the type of  $\mathcal{F}_p^{\gamma,e}$  is non-homogeneous.

For every  $0 \leq k \leq n$ , every state  $p$  and every order- $n$  stack  $s$  whose topmost stack symbol is  $\text{top}_1(s) = \gamma$  with an  $(e + 1)$ -link, we inductively define the following term of order  $\mathbf{k} = (\mathbf{k} + \mathbf{1})^m \rightarrow \cdots \rightarrow \mathbf{n}^m \rightarrow \mathbf{n}$

$$\llbracket s \rrbracket_k^p = \mathcal{F}_p^{\gamma,e} \llbracket \text{collapse}(s) \rrbracket_e^q \llbracket \text{pop}_1(s) \rrbracket_1^q \llbracket \text{pop}_2(s) \rrbracket_2^q \cdots \llbracket \text{pop}_k(s) \rrbracket_k^q$$

where  $\llbracket t \rrbracket_h^q$  is a shorthand for the sequence  $\llbracket t \rrbracket_h^{q_1} \llbracket t \rrbracket_h^{q_2} \cdots \llbracket t \rrbracket_h^{q_m}$ , and if  $\text{pop}_i(s)$  is undefined then we adopt the convention that  $\llbracket \text{pop}_i(s) \rrbracket_i^q$  means  $\underbrace{\text{Void}_i \cdots \text{Void}_i}_m$ .

The preceding definition is well-founded: every stack in the definition of  $\llbracket s \rrbracket_k^p$  has fewer symbols than  $s$ . Intuitively  $\llbracket s \rrbracket_k^p$  represents the top  $k$ -stack of the configuration  $(p, s)$ .

Let  $s$  and  $t$  be order- $n$  stacks with links and let  $1 \leq k \leq n$ . We define  $s$  and  $t$  are **top $_k$** -identical as follows (where  $\text{pop}_k^j(s)$  denotes the stack obtained from  $s$  by  $k$  successive applications of the  $\text{pop}_k$  function):

- $s$  and  $t$  are  $\text{top}_1$ -identical just if  $\text{top}_1(s) = \text{top}_1(t)$ , and  $\text{collapse}(s)$  and  $\text{collapse}(t)$  are  $\text{top}_{e+1}$ -identical where  $\text{top}_1(s)$  has an  $(e + 1)$ -link
- for  $k > 1$ ,  $s$  and  $t$  are  $\text{top}_k$ -identical just if for every  $j \geq 0$ ,  $\text{pop}_{k-1}^j(s)$  is defined if and only if  $\text{pop}_{k-1}^j(t)$  is defined, and if so,  $\text{pop}_{k-1}^j(s)$  and  $\text{pop}_{k-1}^j(t)$  are  $\text{top}_{k-1}$ -identical.

Taking  $j = 0$  in the second item, we note that if  $s$  and  $t$  are  $\text{top}_k$  identical then they are also  $\text{top}_{k'}$ -identical for any  $1 \leq k' \leq k$ . The preceding definition is well-founded because it always refers to stacks with fewer symbols than  $s$  or  $t$ .

**Lemma 5.1.** Let  $s$  and  $t$  be order- $n$  stacks with links, and let  $0 \leq k \leq n - 1$ . If  $s$  and  $t$  are  $\text{top}_{k+1}$ -identical then  $\llbracket s \rrbracket_k^p = \llbracket t \rrbracket_k^p$  for every state  $p$ .

*Proof.* The proof is by induction on the maximum of the respective sizes of  $s$  and  $t$ , and once that is fixed we reason by induction on  $k$ .

The base case of  $s$  and  $t$  containing only the bottom-of-stack symbol is trivial. Assume that the property holds for every pair of stacks, each with no more than  $N$  symbols for some  $N > 0$ , and consider stacks  $s$  and  $t$ , the larger of the two has size  $N + 1$ . Assume that  $s$  and  $t$  are  $\text{top}_{k+1}$ -identical for some  $k \geq 0$ . We now reason by induction on  $k$ .

Suppose  $s$  and  $t$  are  $\text{top}_1$ -identical. By definition, we have that  $\text{top}_1(s) = \text{top}_1(t) = (\gamma, e)$  where  $\gamma \in \Gamma$  and  $1 \leq e \leq n$ , and that  $\text{collapse}(s)$  and  $\text{collapse}(t)$  are  $\text{top}_{e+1}$ -identical. As  $\text{collapse}(s)$  and  $\text{collapse}(t)$  have size bounded by  $N$ , by the induction hypothesis, we have  $\llbracket \text{collapse}(s) \rrbracket_e^q = \llbracket \text{collapse}(t) \rrbracket_e^q$ . Thus it follows immediately that  $\llbracket s \rrbracket_0^p = \llbracket t \rrbracket_0^p$ .

Now take  $k \geq 0$  and assume that the property is established for each  $h \leq k$ . We consider the case of  $k + 1$ . Assume that  $s$  and  $t$  are  $\text{top}_{k+2}$ -identical. It follows that for each  $h \leq k$ , if  $\text{pop}_h(s)$  (equivalently  $\text{pop}_h(t)$ ) is defined then  $\text{pop}_h(s)$  and  $\text{pop}_h(t)$  are  $\text{top}_{h+1}$ -identical, and so, by the induction hypothesis,  $\llbracket \text{pop}_h(s) \rrbracket_h^q = \llbracket \text{pop}_h(t) \rrbracket_h^q$  for every state  $q$ . Because  $s$  and  $t$  are  $\text{top}_{k+2}$ -identical they are also  $\text{top}_1$ -identical and then by definition, we also have  $\text{top}_1(s) = \text{top}_1(t) = (\gamma, e)$  for some  $\gamma \in \Gamma$ , and  $\text{collapse}(s)$  and  $\text{collapse}(t)$  are



Table I. Definition of  $\Xi_\theta$ 

Cases of $op$	Corresponding $\Xi_\theta$ where $\theta = (q, op)$
$push_1^{\gamma', e'}$	$\mathcal{F}_q^{\gamma', e'} \overline{\Psi}_{e'} \langle \mathcal{F}_i^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \mid i \rangle \overline{\Psi}_2 \cdots \overline{\Psi}_n$
$push_k$	$\mathcal{F}_q^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_{(k-1)} \langle \mathcal{F}_i^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_k \mid i \rangle \overline{\Psi}_{(k+1)} \cdots \overline{\Psi}_n$
$pop_k$	$\Psi_{k, q} \overline{\Psi}_{k-1} \cdots \overline{\Psi}_n$
$collapse$	$\Phi_q \overline{\Psi}_{e-1} \cdots \overline{\Psi}_n$

$top_{e+1}$ -identical. As  $collapse(s)$  and  $collapse(t)$  have size bounded by  $N$ , by the induction hypothesis, we have  $\llbracket collapse(s) \rrbracket_e^q = \llbracket collapse(t) \rrbracket_e^q$ .

By definition

$$\llbracket s \rrbracket_{k+1}^p = \mathcal{F}_p^{\gamma, e} \llbracket collapse(s) \rrbracket_e^q \llbracket pop_1(s) \rrbracket_1^q \cdots \llbracket pop_{k+1}(s) \rrbracket_{k+1}^q$$

and

$$\llbracket t \rrbracket_{k+1}^p = \mathcal{F}_p^{\gamma, e} \llbracket collapse(t) \rrbracket_e^q \llbracket pop_1(t) \rrbracket_1^q \cdots \llbracket pop_{k+1}(t) \rrbracket_{k+1}^q$$

Now for any  $n$ -stack  $r$  define  $j_r$  be the maximal  $j$  such that  $pop_{k+1}^j(r)$  is defined. In particular we have  $j_s = j_t$ . If  $j_s = 0$ ,  $\llbracket pop_{k+1}(s) \rrbracket_{k+1}^q = \llbracket pop_{k+1}(t) \rrbracket_{k+1}^q = \text{Void}_{k+1} \cdots \text{Void}_{k+1}$ , and so  $\llbracket s \rrbracket_{k+1}^p = \llbracket t \rrbracket_{k+1}^p$ . If  $j_s > 0$ , note that  $j_{pop_{k+1}(s)} = j_{pop_{k+1}(t)} = j_s - 1$  and  $pop_{k+1}(s)$  and  $pop_{k+1}(t)$  are  $top_{(k+2)}$ -identical. Thus, inductively (on  $j_s$ ), we have  $\llbracket pop_{k+1}(s) \rrbracket_{k+1}^q = \llbracket pop_{k+1}(t) \rrbracket_{k+1}^q$  for every state  $q$ . Hence we conclude that  $\llbracket s \rrbracket_{k+1}^p = \llbracket t \rrbracket_{k+1}^p$ .  $\square$

## 5.2. Associated rewrite rules

With every pair  $\theta = (q, op) \in Q \times Op_n$ , we associate a rewrite rule

$$\mathcal{F}_p^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_n \xrightarrow{\theta} \Xi_\theta$$

where for each  $0 \leq j \leq n$  we have  $\overline{\Psi}_j = \Psi_{j,1} \cdots \Psi_{j,m}$  is a sequence of variables, with each  $\Psi_{j,i} : \mathbf{j}$ ; similarly  $\overline{\Phi} = \Phi_1 \cdots \Phi_m$  is a sequence of variables, with each  $\Phi_i : \mathbf{e}$ .

The shape of  $\Xi_\theta$  depends on  $op$ , as shown in Table I, where  $\langle \mathcal{F}_i^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_k \mid i \rangle$  is a shorthand for the sequence

$$\mathcal{F}_{q_1}^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_k \quad \mathcal{F}_{q_2}^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_k \quad \cdots \quad \mathcal{F}_{q_m}^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_k : \mathbf{k}^m$$

The preceding labelled rewrite rules induce a  $\theta$ -indexed family of outermost labelled one-step transition relations  $\xrightarrow{\theta} \subseteq \mathcal{T}^0(\mathcal{N}_A) \times \mathcal{T}^0(\mathcal{N}_A)$ , where  $\theta$  ranges over  $Q \times Op_n$ . Informally  $M \xrightarrow{\theta} M'$  just if  $M'$  is obtained from  $M$  by replacing the head (equivalently, outermost) non-terminal by the right-hand side of the corresponding rewrite rule in which all formal parameters are in turn replaced by their respective actual parameters. Formally, for each  $\theta = (q, op) \in Q \times Op_n$  and for each corresponding rewrite rule  $\mathcal{F}_p^{\gamma, e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_n \xrightarrow{\theta} \Xi_\theta$ , we have the rule scheme

$$\mathcal{F}_p^{\gamma, e} \overline{L} \overline{M}_1 \cdots \overline{M}_n \xrightarrow{\theta} \Xi_\theta[\overline{L}/\overline{\Phi}, \overline{M}_1/\overline{\Psi}_1 \cdots, \overline{M}_n/\overline{\Psi}_n]$$

where  $\overline{L}, \overline{M}_1, \dots, \overline{M}_n$  range over sequences of terms that respect the type of  $\mathcal{F}_p^{\gamma, e}$ .

Note that each binary relation  $\xrightarrow{\theta}$  is a partial function.

## 5.3. Correctness of the representation

Let  $(p, s)$  be a configuration of an order- $n$  CPDA  $\mathcal{A}$  and let  $\theta = (q, op) \in Q \times Op_n$  be a transition. We say that  $(p, s)$  is  $\theta$ -compatible just if  $\theta$  is an applicable transition from  $(p, s)$  i.e.  $\theta = \delta(p, top_1(s), a)$  for some  $a \in (A \cup \{\varepsilon\})$  and  $op(s)$  is defined. Recall that it is straightforward to transform  $\mathcal{A}$  — without changing its expressivity — so that for every reachable configuration  $(p, s)$ , if  $\theta = (q, op) = \delta(p, top_1(s), a)$  for some  $a \in (A \cup \{\varepsilon\})$  then  $op(s)$  is defined.

The following proposition relates the previous transition system with  $\mathcal{A}$ .

**Proposition 5.2.** Let  $(p, s)$  be a configuration of  $\mathcal{A}$  and  $\theta = (q, op) \in Q \times Op_n$ . If  $(p, s)$  is  $\theta$ -compatible, then  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} t$  if and only if  $t = \llbracket op(s) \rrbracket_n^q$ .

*Proof.* The proof is by a case analysis. Let  $\theta = (q, op) \in Q \times Op_n$  and let  $(p, s)$  be  $\theta$ -compatible. Set  $C^{q_i} = \llbracket collapse(s) \rrbracket_e^{q_i} : \mathbf{e}$ , and  $T_k^{q_i} = \llbracket pop_k(s) \rrbracket_k^{q_i} : \mathbf{k}$  for every  $1 \leq i \leq m$  and every  $1 \leq k \leq n$ . Then

$$\llbracket s \rrbracket_n^p = \mathcal{F}_p^{\gamma, e} \quad C^{q_1} \dots C^{q_m} \quad T_1^{q_1} \dots T_1^{q_m} \quad \dots \quad T_n^{q_1} \dots T_n^{q_m}$$

— Assume  $op = push_1^{\gamma', e'}$ . By definition we have

$$\begin{aligned} \llbracket push_1^{\gamma', e'}(s) \rrbracket_n^q &= \mathcal{F}_q^{\gamma', e'} \llbracket collapse(push_1^{\gamma', e'}(s)) \rrbracket_{e'}^{\bar{q}} \\ &\quad \llbracket pop_1(push_1^{\gamma', e'}(s)) \rrbracket_1^{\bar{q}} \dots \llbracket pop_n(push_1^{\gamma', e'}(s)) \rrbracket_n^{\bar{q}} \end{aligned}$$

We have  $collapse(push_1^{\gamma', e'}(s)) = pop_{e'}(s)$ , hence  $\llbracket collapse(push_1^{\gamma', e'}(s)) \rrbracket_{e'}^{\bar{q}} = T_{e'}^{\bar{q}}$ . Further we have  $pop_1(push_1^{\gamma', e'}(s)) = s$ , hence  $\llbracket pop_1(push_1^{\gamma', e'}(s)) \rrbracket_1^{q_i} = \mathcal{F}_{q_i}^{\gamma, e} C^{\bar{q}} T_1^{\bar{q}}$ . Finally, for each  $j > 1$ , we have  $pop_j(push_1^{\gamma', e'}(s)) = pop_j(s)$ , hence

$$\llbracket pop_j(push_1^{\gamma', e'}(s)) \rrbracket_j^{\bar{q}} = T_j^{\bar{q}}.$$

Therefore, we have

$$\llbracket push_1^{\gamma', e'}(s) \rrbracket_n^q = \mathcal{F}_q^{\gamma', e'} T_{e'}^{\bar{q}} (\mathcal{F}_{q_1}^{\gamma, e} C^{\bar{q}} T_1^{\bar{q}}) \dots (\mathcal{F}_{q_m}^{\gamma, e} C^{\bar{q}} T_1^{\bar{q}}) T_2^{\bar{q}} \dots T_n^{\bar{q}}$$

On the other hand, it follows syntactically from the definition of  $\xrightarrow{\theta}$  that the right-hand side of the preceding equation is the term  $t$  such that  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} t$ . Hence one has  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} \llbracket push_1^{\gamma', e'}(s) \rrbracket_n^q$ .

— Assume  $op = push_k$ . By definition we have

$$\llbracket push_k(s) \rrbracket_n^q = \mathcal{F}_q^{\gamma, e} \llbracket collapse(push_k(s)) \rrbracket_e^{\bar{q}} \llbracket pop_1(push_k(s)) \rrbracket_1^{\bar{q}} \dots \llbracket pop_n(push_k(s)) \rrbracket_n^{\bar{q}}$$

Note that we used the fact that the  $top_1$  element in  $push_k(s)$  is the same as that in  $s$  i.e. it is  $\gamma$  and has an  $(e+1)$ -link. Now if  $e \leq k$ ,  $collapse(push_k(s))$  and  $collapse(s)$  are  $top_{e+1}$ -identical; hence, thanks to Lemma 5.1

$$\llbracket collapse(push_k(s)) \rrbracket_e^{q_i} = \llbracket collapse(s) \rrbracket_e^{q_i} = C^{q_i}.$$

If  $e > k$ ,  $collapse(s) = collapse(push_k(s))$ ; hence we also have

$$\llbracket collapse(push_k(s)) \rrbracket_e^{q_i} = C^{q_i}.$$

Next, for  $j < k$ ,  $pop_j(push_k(s))$  and  $pop_j(s)$  are  $top_{j+1}$ -identical; hence, thanks to Lemma 5.1,  $\llbracket pop_j(push_k(s)) \rrbracket_j^{q_i} = \llbracket pop_j(s) \rrbracket_j^{q_i} = T_j^{q_i}$ . Further we have  $pop_k(push_k(s)) = s$ ; hence  $\llbracket pop_k(push_k(s)) \rrbracket_k^{q_i} = \mathcal{F}_{q_i}^{\gamma, e} C^{\bar{q}} T_1^{\bar{q}} \dots T_k^{\bar{q}}$  for every  $1 \leq i \leq m$ . Finally, for every

$j > k$ , we have  $pop_j(push_k(s)) = pop_j(s)$ ; hence  $\llbracket pop_j(push_k(s)) \rrbracket_j^{\bar{q}} = T_j^{\bar{q}}$ . Therefore we have

$$\llbracket push_k(s) \rrbracket_n^q = \mathcal{F}_q^{\gamma, e} C^{\bar{q}} T_1^{\bar{q}} \cdots T_{k-1}^{\bar{q}} \\ (\mathcal{F}_{q_1}^{\gamma, e} C^{\bar{q}} T_1^{\bar{q}} \cdots T_k^{\bar{q}}) \cdots (\mathcal{F}_{q_m}^{\gamma, e} C^{\bar{q}} T_1^{\bar{q}} \cdots T_k^{\bar{q}}) T_{k+1}^{\bar{q}} \cdots T_n^{\bar{q}}$$

On the other hand, it follows syntactically from the definition of  $\xrightarrow{\theta}$  that the right-hand side of the preceding equation is the term  $t$  such that  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} t$ . Hence one has  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} \llbracket push_k(s) \rrbracket_n^q$ .

— Assume  $op = pop_k$ . By definition we have

$$\llbracket pop_k(s) \rrbracket_n^q = \mathcal{F}_q^{\gamma', e'} \llbracket collapse(pop_k(s)) \rrbracket_e^{\bar{q}} \llbracket pop_1(pop_k(s)) \rrbracket_1^{\bar{q}} \cdots \llbracket pop_n(pop_k(s)) \rrbracket_n^{\bar{q}}$$

where the  $top_1$  element in  $pop_k(s)$  is a  $\gamma'$  and has an  $(e' + 1)$ -link. It follows that

$$\llbracket pop_k(s) \rrbracket_n^q = \llbracket pop_k(s) \rrbracket_k^q \llbracket pop_{k+1}(pop_k(s)) \rrbracket_{k+1}^{\bar{q}} \cdots \llbracket pop_n(pop_k(s)) \rrbracket_n^{\bar{q}}$$

For every  $j > k$ , we have  $pop_j(pop_k(s)) = pop_j(s)$ , hence  $\llbracket pop_j(pop_k(s)) \rrbracket_j^{\bar{q}} = T_j^{\bar{q}}$ . Therefore we have  $\llbracket pop_k(s) \rrbracket_n^q = T_k^q T_{k+1}^{\bar{q}} \cdots T_n^{\bar{q}}$ . On the other hand, it follows syntactically from the definition of  $\xrightarrow{\theta}$  that the right-hand side of the preceding equation is the term  $t$  such that  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} t$ . Hence one has  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} \llbracket pop_k(s) \rrbracket_n^q$ .

— Assume  $op = collapse$ . By definition we have

$$\llbracket collapse(s) \rrbracket_n^q = \mathcal{F}_q^{\gamma', e'} \llbracket collapse(collapse(s)) \rrbracket_e^{\bar{q}} \\ \llbracket pop_1(collapse(s)) \rrbracket_1^{\bar{q}} \cdots \llbracket pop_n(collapse(s)) \rrbracket_n^{\bar{q}}$$

where the  $top_1$  element in  $collapse(s)$  is  $\gamma'$  and has an  $(e' + 1)$ -link. Equivalently, one has

$$\llbracket collapse(s) \rrbracket_n^q = \llbracket collapse(s) \rrbracket_e^q \llbracket pop_{e+1}(collapse(s)) \rrbracket_{e+1}^{\bar{q}} \cdots \llbracket pop_n(collapse(s)) \rrbracket_n^{\bar{q}}$$

For every  $j > e$  we have  $pop_j(collapse(s)) = pop_j(s)$ , hence  $\llbracket pop_j(collapse(s)) \rrbracket_j^{\bar{q}} = T_j^{\bar{q}}$ . Therefore we have  $\llbracket collapse(s) \rrbracket_n^q = C^q T_{e+1}^{\bar{q}} \cdots T_n^{\bar{q}}$ . On the other hand, it follows syntactically from the definition of  $\xrightarrow{\theta}$  that the right-hand side of the preceding equation is the term  $t$  such that  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} t$ . Hence one has  $\llbracket s \rrbracket_n^p \xrightarrow{\theta} \llbracket collapse(s) \rrbracket_n^q$ .

□

We define a relation  $\sim$  between configurations of  $\mathcal{A}$  and ground-type terms generated from symbols from the set

$$\mathcal{N} = \{\mathcal{F}_p^{\gamma, e} \mid \gamma \in \Gamma, 1 \leq e \leq n, p \in Q\} \cup \{\text{Void}_i \mid 1 \leq i \leq n\},$$

defined by  $(p, s) \sim \llbracket s \rrbracket_n^p$ . Then  $\sim$  is a bisimulation.

#### 5.4. The recursion scheme $G_{\mathcal{A}}$ determined by a CPDA $\mathcal{A}$

Fix some integer  $d \geq 1$  and let  $[d]$  denote  $\{1, \dots, d\}$ . Fix an  $n$ -CPDA  $\mathcal{A} = \langle [d] \cup \{\varepsilon\}, \Gamma, Q, \delta, q_1 \rangle$  and a function  $\rho : Q \times \Gamma \rightarrow \Sigma$ , and let  $Q = \{q_1, \dots, q_m\}$ . Let  $t$  be the tree generated by  $\mathcal{A}$  and  $\rho$  as defined in Section 3.3.

We define from  $\mathcal{A}$  and  $\rho$  an order- $n$  recursion scheme whose value tree is  $t$ . The main idea here is to rely on the previous term representation of configurations of  $\mathcal{A}$ . Indeed, what we did so far was to define an  $([d] \cup \{\varepsilon\})$ -edge-labelled transition system whose elements

are finite terms of ground type and to prove that it is bisimilar (in the usual sense) with  $\text{Graph}(\mathcal{A})$ . Hence, it suffices to design a recursion scheme that mimics the dynamics of the previous term-rewrite system, i.e. such that its value tree is the tree obtained from the previous transition system by unfolding, contracting the  $\varepsilon$ -transitions, and labelling (according to the head non terminal).

**Definition 5.3.** The order- $n$  recursion scheme determined by  $\mathcal{A}$  and  $\rho$  is defined to be  $G_{\mathcal{A},\rho} = \langle \Sigma, \mathcal{N}, \mathcal{R}, S \rangle$  (written  $G_{\mathcal{A}}$  if  $\rho$  is clear) where

$$\mathcal{N} = \{\mathcal{F}_p^{\gamma,e} \mid \gamma \in \Gamma, 1 \leq e \leq n, p \in Q\} \cup \{\text{Void}_i \mid 1 \leq i \leq n\}$$

consists of those non-terminals as introduced in Section 5.1, and the rules in  $\mathcal{R}$  are as follows

$$\begin{aligned} S &\longrightarrow \rho(q_1, \perp) && \text{if } ar(\rho(q_1, \perp)) = 0 \\ S &\longrightarrow \mathcal{F}_{q_1}^{\perp,1} \overline{\text{Void}_1} \cdots \overline{\text{Void}_n} && \text{otherwise} \end{aligned}$$

and

$$\begin{aligned} \mathcal{F}_p^{\gamma,e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_n &\longrightarrow \Xi_\theta && \text{if } \delta(p, \gamma, \varepsilon) = \theta \text{ is defined} \\ \mathcal{F}_p^{\gamma,e} \overline{\Phi} \overline{\Psi}_1 \cdots \overline{\Psi}_n &\longrightarrow \rho(p, \gamma) \Xi_{\theta_1} \cdots \Xi_{\theta_r} && \text{otherwise, where } r = ar(\rho(p, \gamma)) \text{ where} \\ &&& \theta_i = \delta(p, \gamma, i) \text{ for } i = 1, \dots, r. \end{aligned}$$

Note that in the definition, we need to distinguish those states  $p$  and stack symbols  $\gamma$  where  $\{a \in [d] \cup \{\varepsilon\} \mid (p, \gamma, a) \in \text{Dom}(\delta)\} = \emptyset$ . Indeed, one still needs to produce a terminal for them as they correspond to productive leaves in the tree obtained from  $\text{Graph}(\mathcal{A})$  by unfolding and contracting the  $\varepsilon$ -transitions.

We are now in a position to state the major result of the section.

**Theorem 5.4 (Equi-Expressivity 1).** Let  $\mathcal{A}$  be a tree-generating CPDA, and  $G_{\mathcal{A}}$  be the recursion scheme determined by  $\mathcal{A}$ . Then the CPDA and the recursion scheme generate the same  $\Sigma$ -labelled tree.

*Proof.* The proof follows from Proposition 5.2, the definition of  $G_{\mathcal{A}}$  and the way one generates a tree from a CPDA.

The key idea here is to give a precise description of the terms  $t$  such that  $S \xrightarrow{*}_{G_{\mathcal{A}}} t$  where  $\xrightarrow{*}_{G_{\mathcal{A}}}$  denotes the transitive closure of  $\rightarrow_{G_{\mathcal{A}}}$ .

Let  $s$  and  $t$  be two finite terms of ground type. We say that  $s$  is a subterm of  $t$  if either  $s = t$ , or there exist  $f \in \Sigma$  and  $i \in \{1, \dots, \ell\}$  such that  $t = ft_1 \cdots t_\ell$  and  $s$  is a subterm of  $t_i$ . Note that, in the sequel, we implicitly distinguish two copies of a term that appears as subterm in different parts of a given term. More formally, every subterm  $s$  of a term  $t$  has a location, denoted  $location_t(s)$  (or simply  $location(s)$  if  $t$  is clear), which is a sequence, where  $\cdot$  denotes concatenation of sequences. It is defined by

$$location_t(s) := \begin{cases} \varepsilon & \text{if } s = t \\ f \cdot i \cdot location_{t_i}(s) & \text{otherwise, where } t = f t_1 \cdots t_\ell \text{ and} \\ & s \text{ is a subterm of } t_i \end{cases}$$

One can easily characterise those terms that can be derived from  $S$  in  $\rightarrow_{G_{\mathcal{A}}}$ . Indeed we have  $S \xrightarrow{*}_{G_{\mathcal{A}}} t$  if and only if either  $t = S$  or for every subterm  $t'$  of  $t$  such that

$location_t(t') = f_1 \cdot a_1 \cdots f_\ell \cdot a_\ell \in (\Sigma \cdot \{1, \dots, d\})^*$  with  $\ell \geq 0$ , we have  $t' = \llbracket s \rrbracket_n^p$  for some configuration  $(p, s)$  in  $\text{Graph}(\mathcal{A})$  such that there exist a sequence  $(p_0, s_0), \dots, (p_{\ell+1}, s_{\ell+1})$  of configurations of  $\text{Graph}(\mathcal{A})$  and numbers  $k_1, \dots, k_{\ell+1} \geq 0$  such that

- $(p_0, s_0) = (q_1, \perp_n)$  is the initial configuration;
- $(p_0, s_0) \xrightarrow{\varepsilon^{k_1}} (p_1, s_1)$ ;
- $(p_i, s_i) \xrightarrow{a_i \varepsilon^{k_{i+1}}} (p_{i+1}, s_{i+1})$  for all  $1 \leq i \leq \ell - 1$ ;
- $(p_\ell, s_\ell) \xrightarrow{a_\ell \varepsilon^{k_{\ell+1}}} (p_{\ell+1}, s_{\ell+1})$ ;
- $(p_{\ell+1}, s_{\ell+1}) = (p, s)$ ;
- $\rho(p_i, top_1(s_i)) = f_i$  for all  $1 \leq i \leq \ell$ .

The previous characterisation is proved directly by an induction on the number of rewrite rules applied to derive  $t$  from  $S$ : the base case is immediate, and the inductive step follows from Proposition 5.2.

It follows from the previous lemma and the definition of a tree generated by a CPDA that the value tree of  $G_{\mathcal{A}}$  is the tree generated by  $\mathcal{A}$  and  $\rho$ .  $\square$

## 6. FROM RECURSION SCHEMES TO CPDA

The previous section demonstrates that higher-order recursion schemes are at least as expressive as CPDAs. In this section we prove the converse. Hence, CPDAs and recursion schemes are equi-expressive. A number of related results can be found in the literature, but an exact correspondence with general recursion schemes has never been proved before. Notably, in order to establish a correspondence between recursion schemes and higher-order PDAs, Damm and Goerdt (for word languages [Damm 1982; Damm and Goerdt 1986]) as well as Knapik, Niwiński and Urzyczyn (for labelled trees [Knapik et al. 2002]), have had to impose constraints on the shape of the former (called derived types and safety respectively) and their translation techniques relied on the restrictions in a crucial way.

Our translation from recursion schemes to CPDA is novel: we transform an arbitrary order- $n$  recursion scheme  $G$  to an order- $n$  collapsible pushdown automaton  $\mathcal{A}_G$  that computes the traversals over the computation tree  $\lambda(G)$  (in the sense of [Ong 2006a; Ong 2006b]). The game-semantic interpretation of  $G$  is an innocent strategy (in the sense of [Hyland and Ong 2000]), which coincides with the value tree  $\llbracket G \rrbracket$  of  $G$ , so that paths in the value tree are plays of the strategy. Traversals over the computation tree are just (appropriate representations of) uncoverings [Hyland and Ong 2000] of paths in the value tree.

### 6.1. Long transform, graph representing a recursion scheme, traversals

We first introduce several concepts we need for the rest of the section.

We write  $[n]$  as a shorthand for  $\{1, \dots, n\}$  and  $[n]_0$  for  $\{0, \dots, n\}$ . Fix a ranked alphabet  $\Sigma$ . Typically<sup>4</sup>  $\text{Dir}(f) = [ar(f)]$  and we always have  $|\text{Dir}(f)| = ar(f)$  for each  $\Sigma$ -symbol  $f$ .

We recall the long transform of a recursion scheme as introduced in [Ong 2006b]. Fix a recursion scheme  $G$ . Rules of the new recursion scheme  $\overline{G}$  (which, we shall see, can be regarded as order 0) are obtained from those of  $G$  by applying the following four operations in turn, which is called long transform. For each  $G$ -rule:

1. Expand the right-hand side to its  $\eta$ -long form. I.e. we hereditarily  $\eta$ -expand every sub-term – even if it is of ground type – provided it occurs in an operand position. Note that each term  $s \in \mathcal{T}(\Sigma \cup \mathcal{N} \cup \{\xi_1, \dots, \xi_l\})$  can be written uniquely as  $\dagger s_1 \cdots s_m$  where  $\dagger$  is either a variable (i.e. some  $\xi_j$ ) or a non-terminal or a terminal. Suppose

<sup>4</sup>The only exception is the symbol  $@_A$  of the auxiliary alphabet  $\Lambda_G$ , where we have  $\text{Dir}(@_A) = [ar(@_A) - 1]_0$ .

$\dagger s_1 \cdots s_m : (A_1, \dots, A_n, o)$ . We define

$$\lceil \dagger s_1 \cdots s_m \rceil = \lambda \bar{\varphi}. \lceil s_1 \rceil \cdots \lceil s_m \rceil \lceil \varphi_1 \rceil \cdots \lceil \varphi_n \rceil$$

where  $\bar{\varphi}$  is a list  $\varphi_1 \cdots \varphi_n$  of (fresh) pairwise-distinct variables (which is a null list iff  $n = 0$ ) of types  $A_1, \dots, A_n$  respectively, none of which occurs free in  $\lceil \dagger s_1 \rceil \cdots \lceil s_m \rceil$ .

For example the  $\eta$ -long form of  $ga : o$  is  $\lambda.g(\lambda.a)$ ; we shall see that the “dummy lambda-abstraction”<sup>5</sup>  $\lambda.a$  (that binds a null list of variable) plays a useful rôle in the syntactic representation of the game semantics of a recursion scheme.

2. Insert long-apply symbols  $@_A$ : Replace each ground-type subterm of the shape  $D e_1 \cdots e_n$ , where  $D : (A_1, \dots, A_n, o)$  is a non-terminal and  $n \geq 1$  (i.e.  $D$  has order at least 1), by  $@_A D e_1 \cdots e_n$  where  $A = ((A_1, \dots, A_n, o), A_1, \dots, A_n, o)$  and  $@_A : A$ . In the following, we shall often omit the type tag  $A$  from  $@_A$ , as it is uniquely determined by the respective types of  $D, e_1, \dots, e_n$ .
3. Curry the rewrite rule. I.e. we transform the rule  $F \varphi_1 \cdots \varphi_n \rightarrow \lambda.e'$  to

$$F \rightarrow \lambda \varphi_1 \cdots \varphi_n.e'$$

In case  $n = 0$ , note that the curried rule has the form  $F \rightarrow \lambda.e'$ .

4. Rename bound variables afresh, so that any two variables that are bound by different lambdas have different names.

Example 6.1. We revisit the recursion scheme of Example 2.1 and illustrate the long transform:

$$G : \begin{cases} S \rightarrow H a \\ H z \rightarrow F(g z) \\ F \varphi \rightarrow \varphi(\varphi(F h)) \end{cases} \mapsto \bar{G} : \begin{cases} S \rightarrow \lambda.@ H(\lambda.a) \\ H \rightarrow \lambda z.@ F(\lambda y.g(\lambda.z)(\lambda.y)) \\ F \rightarrow \lambda \varphi.\varphi(\lambda.\varphi(\lambda.@ F(\lambda x.h(\lambda.x)))) \end{cases}$$

For instance, the right hand side of the third rule is  $\lambda.\varphi(\lambda.\varphi(\lambda.F(\lambda x.h(\lambda.x))))$  after the first step, and  $\lambda.\varphi(\lambda.\varphi(\lambda.@ F(\lambda x.h(\lambda.x))))$  after the second step.

For every recursion scheme  $G$ , the system of transformed rules in  $\bar{G}$  defines an order-0 recursion scheme – called the long transform of  $G$  – with respect to an enlarged ranked alphabet  $\Lambda_G$ , which is  $\Sigma$  augmented by certain variables and lambdas (of the form  $\lambda \bar{\xi}$  which is a short hand for  $\lambda \xi_1 \cdots \xi_n$  where  $n \geq 0$ ) but regarded as terminals. The alphabet  $\Lambda_G$  is a finite subset of the set

$$\underbrace{\Sigma \cup \text{Var} \cup \{ @_A \mid A \in \text{ATypes} \}}_{\text{Non-lambdas}} \cup \underbrace{\{ \lambda \bar{\xi} \mid \bar{\xi} \subseteq \text{Var} \}}_{\text{Lambdas}}$$

where ATypes is the set of types of the shape  $((A_1, \dots, A_n, o), A_1, \dots, A_n, o)$  with  $n \geq 1$ . We rank the symbols in  $\Lambda_G$  as follows:

- variable symbol  $\varphi : (A_1, \dots, A_n, o)$  in  $\text{Var}$  has arity  $n$
- long-apply symbol  $@_A$  where  $A = ((A_1, \dots, A_n, o), A_1, \dots, A_n, o)$  has arity  $n + 1$
- lambda symbol  $\lambda \bar{\xi}$  has arity 1, for every list of variables  $\bar{\xi} \subseteq \text{Var}$ .

<sup>5</sup>To our knowledge, Colin Stirling was the first to use a tree representation of lambda terms in which “dummy lambdas” are employed; see his paper [Stirling 2005]. Motivated by property-checking games in Verification, he has introduced a game that is played over such trees as a characterisation of higher-order matching [Stirling 2009].

Further, for  $f \in \Lambda_G$ , we define

$$\text{Dir}(f) = \begin{cases} [ar(@_A) - 1]_0 & \text{if } f = @_A \\ [ar(f)] & \text{otherwise} \end{cases}$$

For technical reasons (to be clarified shortly), the leftmost child of an @-labelled node  $\alpha$  is in direction 0 (i.e. it is  $\alpha$ 's 0-child); for all other nodes, the leftmost child is in direction 1. The non-terminals of  $\overline{G}$  are exactly those of  $G$ , except that each is assigned a new type, namely,  $o$ . We can now define the computation tree  $\lambda(G)$  to be the value tree  $\llbracket \overline{G} \rrbracket$  of the order-0 recursion scheme  $\overline{G}$ . It follows that  $\lambda(G)$  is a regular tree<sup>6</sup>.

A  $\Lambda$ -labelled rooted deterministic digraph (or DDG, for short) is a quadruple

$$\mathcal{K} = \langle V, E, l, v_0 \rangle$$

where  $\langle V, E \rangle$  is a finite digraph vertex-labelled by the function  $l : V \rightarrow \Lambda$  with  $\Lambda$  a ranked alphabet, such that each vertex  $v \in V$  has as many successors as the arity of  $l(v)$ , and each of these successors are ordered; and  $v_0 \in V$  is a distinguished vertex called the root. We denote by  $E_i(v)$  the unique  $i$ -th successor of  $v$  for  $i = 1, \dots, ar(l(v))$ .

It is easy to see that every finite  $\Lambda$ -labelled tree can be presented as a DDG.

The unfolding of  $\mathcal{K}$  is the  $\Lambda$ -labelled ranked tree  $t : \text{Dom}(t) \rightarrow \Lambda$  such that  $\text{Dom}(t)$  is the set of finite paths in  $\mathcal{K}$  starting from the root  $v_0$ , and  $t(v_0 \cdots v_k) = l(v_k)$  where the  $i$ -th child (when defined) of node  $v_1 \cdots v_k$  is  $v_1 \cdots v_k E_i(v_k)$ . This definition (canonically) associates vertices in  $\mathcal{K}$  with nodes in  $t$ : the node  $\varepsilon$  is mapped to  $v_0$ , and the node  $v_1 \cdots v_k$  ( $k \geq 1$ ) is mapped to  $v_k$ . This map extends to an association of node sequences in  $t$  with vertex sequences in  $\mathcal{K}$ . When restricted to paths in  $t$  and paths in  $\mathcal{K}$  starting from the root, we obtain a bijection.

Fix a higher-order recursion scheme  $G$  and an associated long transform  $\overline{G}$ . We define the HORS graph  $\mathbf{Gr}(G)$  to be the  $\Lambda_G$ -labelled DDG determined by  $G$

$$\mathbf{Gr}(G) = \langle V, E \subseteq V \times V, \lambda_G : V \rightarrow \Lambda_G, v_0 \in V \rangle$$

that is obtained by the following procedure:

- (1) First we define the ranked alphabet  $\Lambda_{G, \overline{G}} = \Lambda_G \cup \mathcal{N}_{\overline{G}}$  where each symbol in  $\mathcal{N}_{\overline{G}}$  (i.e. a non-terminal of  $\overline{G}$ ) is given arity 0.
- (2) For each  $\overline{G}$ -rule (say)  $F \rightarrow \lambda\varphi_1 \cdots \varphi_n.e$ , the corresponding  $\Lambda_{G, \overline{G}}$ -labelled DDG

$$\mathcal{D}_F = \langle V_F, E^F \subseteq V_F \times V_F, l_F : V_F \rightarrow \Lambda_{G, \overline{G}}, rt_F \rangle$$

given by the  $\Lambda_{G, \overline{G}}$ -labelled tree that is determined by the right-hand side of the rule, namely,  $\lambda\varphi_1 \cdots \varphi_n.e$ . In particular,  $rt_F$  is the root of that tree and we have  $l_F(rt_F) = \lambda\varphi_1 \cdots \varphi_n$  with reference to the rule  $F$  given above.

- (3) First for each  $F$  in  $\mathcal{N}_{\overline{G}}$  we define

$$\mathcal{E}_F = \bigcup_{H \in \mathcal{N}_{\overline{G}}} l_H^{-1}(\{F\}) \quad \text{and} \quad \mathcal{E} = \bigcup_{F \in \mathcal{N}_{\overline{G}}} \mathcal{E}_F$$

Then  $\mathbf{Gr}(G)$  is intuitively obtained by taking the disjoint union of the underlying digraphs of  $\mathcal{D}_F$  and then merging with  $rt_F$  all those vertices that belong to same  $\mathcal{E}_F$ , as  $F$  ranges over  $\mathcal{N}_{\overline{G}}$ .

Formally we have the following.

<sup>6</sup>An infinite tree is regular if and only if it contains finitely many different infinite subtrees. Equivalently, an infinite tree is regular if it can be obtained by unfolding a finite directed graph.



— The vertices  $V$  of  $\text{Gr}(G)$  are defined by

$$V = \bigcup_{F \in \mathcal{N}_{\overline{G}}} V_F \setminus \bigcup_{F \in \mathcal{N}_{\overline{G}}} \mathcal{E}_F$$

— The root  $v_0$  of  $\text{Gr}(G)$  is  $rt_S$ , where  $S$  is the start symbol of  $\overline{G}$ .

— The vertex-labels are defined by

$$\lambda_G(v) = \begin{cases} l_F(rt_F) & \text{if } v = \mathcal{E}_F \text{ for some } F \in \mathcal{N}_{\overline{G}} \\ l_H(v) & \text{otherwise, where } v \text{ is a vertex in } V_H \end{cases}$$

— The edges  $E$  of  $\text{Gr}(G)$  are defined by

$$E = \left( \bigcup_{F \in \mathcal{N}_{\overline{G}}} E_F \right) \setminus \{(v, v') \mid v \in \mathcal{E} \text{ or } v' \in \mathcal{E}\} \cup \bigcup_{F' \in \mathcal{N}_{\overline{G}}} \{(rt_{F'}, v') \mid (v, v') \in E_{F'} \text{ and } v \in \mathcal{E}_F\} \cup \{(v', rt_{F'}) \mid (v', v) \in E_{F'} \text{ and } v \in \mathcal{E}_F\}$$

and the edge-labels of  $\text{Gr}(G)$  are inherited from the edge-labels of the component DDGs  $\mathcal{D}_F$  according to how vertices and edges were merged.

In the following, we shall only concern ourselves with the connected component of  $\text{Gr}(G)$  that contains the root node (and assume that  $\text{Gr}(G)$  is that connected component)<sup>7</sup>. It follows from the definitions that unfolding  $\text{Gr}(G)$  gives the computation tree  $\lambda(G)$ .

Example 6.2. We revisit the recursion scheme of examples 2.1 and 6.1. The graph  $\text{Gr}(G)$  is given in Figure 2.

Fix a HORS graph  $\text{Gr}(G) = \langle V, E, \lambda_G, v_0 \rangle$ . We shall call a vertex of  $\text{Gr}(G)$  prime just if it is the 0-child<sup>8</sup> of a @-labelled vertex. By construction, a prime vertex is labelled by a lambda. We define the depth of a vertex to be the length of the shortest path from the root to the vertex (so that the root has depth 0). Let  $u$  be a vertex. We define  $\text{pred}(u) = \{u' \in V : (u', u) \in E\}$  i.e. the set of predecessors of  $u$ . For every vertex  $u$  labelled by a variable  $\varphi_i$  (say), its binder, written  $\text{binder}(u)$ , is the vertex that is labelled  $\lambda\overline{\varphi}$ , where  $\overline{\varphi}$  is a list of variables that contains  $\varphi_i$ . (Since bound variables are renamed to prevent any clash in the construction of  $\overline{G}$ , every variable vertex in  $\text{Gr}(G)$  has a unique binder.) We say that  $u$  is the  $i$ -parameter of  $\text{binder}(u)$  just if  $\varphi_i$  is the  $i$ th-item of the list  $\overline{\varphi}$ . The span of the variable vertex  $u$  is defined to be the depth of  $u$  minus the depth of  $\text{binder}(u)$ .

We note the following features of HORS graphs:

- (i) Except the root and possibly some prime vertices, every vertex  $u$  has a unique predecessor  $v$ . For  $j > 0$ , if  $u$  is the  $j$ -child of  $v$ , we say that  $u$  is a  $j$ -child. If  $u$  is prime then it is the 0-child of all its predecessors and we then say that  $u$  is a 0-child. Indeed, a vertex is a 0-child if and only if it is prime.
- (ii) For every vertex  $u$ , there is a unique shortest path from  $\text{binder}(u)$  to  $u$ , and this path does not contain any prime vertex.

For convenience, and whenever it is safe to do so, we shall confuse a vertex  $u$  with its  $\Lambda_G$ -label  $\lambda_G(u)$ .

<sup>7</sup>Note that if  $\text{Gr}(G)$  contains several connected components, some rules in  $G$  are never used to produce  $\llbracket G \rrbracket$ .

<sup>8</sup>The leftmost child of a @-labelled vertex is the latter's 0-child (i.e. the child is at the end of a 0-labelled edge); the leftmost child of any other vertex is a 1-child.

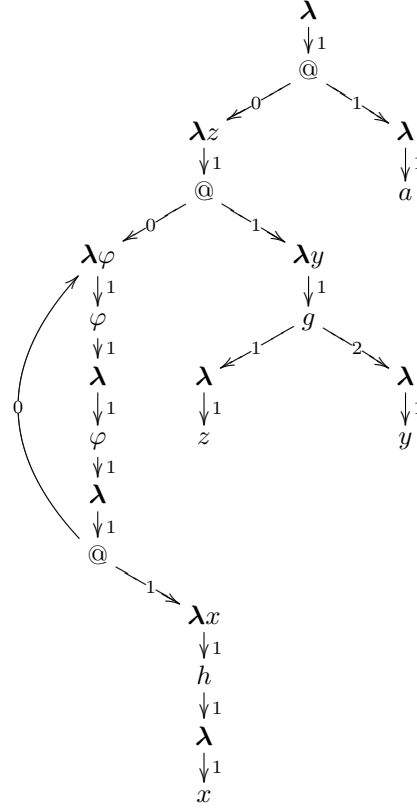


Fig. 2. The graph determined by the order-2 recursion scheme from examples 2.1 and 6.1.

We now define several notions regarding the computation tree. For simplicity, we shall refer to a node labelled by some lambda (resp. variable) as a lambda node (resp. a variable node).

The notion of binder can also be defined for the computation tree. Indeed, let  $n$  be some node in  $\lambda(G)$  labelled by a variable  $\xi$ . We say that  $n$  is bound by the node  $n'$  (equivalently that  $n'$  is the binder of  $n$ ) just in case  $n'$  is the largest prefix of  $n$  that is labelled by a lambda symbol  $\lambda\bar{\xi}$  for some list  $\bar{\xi}$  that contains  $\xi$ .

Binders allow us to define a binary relation  $\vdash_i$  over the set of nodes of  $\lambda(G)$ , called enabling (we read  $n \vdash_i n'$  as “ $n$   $i$ -enables  $n'$ ”, or “ $n'$  is  $i$ -enabled by  $n$ ”), as follows.

- Every lambda node, except the root, is  $i$ -enabled by its parent node in  $\lambda(G)$ , where the former is the  $i$ -child of the latter.
- Every variable node (labelled by some  $\xi_i$ , say) is  $i$ -enabled by its binder (labelled by some  $\bar{\xi}$ , say) where  $\xi_i$  is the  $i$ -th element of the list  $\bar{\xi}$ .

We say that a node of  $\lambda(G)$  is initial if it is not enabled by any node. It follows from the definition that the initial nodes are the root-node (necessarily labelled by the lambda symbol  $\lambda$ ), and all nodes labelled by a long-apply or a  $\Sigma$ -symbol.

Enabling permits us to define the notion of justified sequence over the computation tree. A justified sequence over  $\lambda(G)$  is a possibly infinite, lambda / non-lambda alternating sequence of nodes that satisfies the pointer condition: Each non-initial node  $n$  that occurs

Table II. Rules for defining traversals.

<p>(Root). The singleton sequence, comprising the root node <math>\varepsilon</math>, is a traversal.</p> <p>(App). If <math>t n</math> is a traversal for some sequence <math>t</math> and some node <math>n</math> labelled by <math>@</math>, so is <math>t n \overset{\curvearrowright}{\leftarrow} n'</math> for some node <math>n'</math>. Note that <math>n'</math> is always a lambda node.</p> <p>(Sig). If <math>t n</math> is a traversal for some sequence <math>t</math> and some node <math>n</math> labelled by a <math>\Sigma</math>-symbol <math>f</math>, so is <math>t n \overset{\curvearrowright}{\leftarrow} n'</math> for each <math>1 \leq i \leq ar(f)</math> and some node <math>n'</math>. Note that <math>n'</math> is always labelled by a dummy <math>\lambda</math>.</p> <p>(Var). If <math>t n n' \dots n''</math> is a traversal for some sequence <math>t</math> and some lambda node <math>n'</math> (labelled by some <math>\lambda \bar{\xi}</math>) and some variable node <math>n''</math> (labelled by <math>\xi_i</math>, the <math>i</math>-th variable in <math>\bar{\xi}</math>), so is <math>t n \overset{\curvearrowright}{\leftarrow} n' \dots n'' n'''</math> for each node <math>n'''</math>. Note that <math>n'''</math> is always a lambda node.</p> <p>(Lam). If <math>t n</math> is a traversal for some sequence <math>t</math> and some lambda node <math>n</math>, so is <math>t n n'</math> where <math>n'</math> is the 1-child of <math>n</math>. By a straightforward induction, <math>\ulcorner t n \urcorner</math> is a path in the tree <math>\lambda(G)</math>; if <math>n'</math> is labelled by a variable (as opposed to <math>@</math>) then its pointer in <math>t n n'</math> is determined by the condition that <math>\ulcorner t n n' \urcorner</math> is a path in <math>\lambda(G)</math>.</p>
--

in it has a pointer to some earlier node-occurrence  $n_0$  in the sequence such that  $n_0 \vdash_i n$  for some  $i$ . We say that the node-occurrence  $n$  is justified by the node-occurrence  $n_0$  in the sequence. We use the notation

$$\dots n_0 \overset{\curvearrowright}{\leftarrow} \dots n \dots$$

to mean that  $n$  points to  $n_0$  and that  $n_0 \vdash_i n$  holds. We say that  $n$  is  $i$ -justified by  $n_0$ , or  $n$  has a  $i$ -pointer to  $n_0$  in the justified sequence. Let us stress that a justified sequence need not be a path.<sup>9</sup>

Let  $\mathbf{t}$  be a justified sequence and let  $n$  be some occurrence of a node in  $\mathbf{t}$ . Then we write  $\mathbf{t}_{\leq n}$  (resp.  $\mathbf{t}_{< n}$ ) to mean the prefix of  $\mathbf{t}$  truncated at and including (resp. excluding) the node  $n$ .

We are now ready to introduce traversals. Traversals over the computation tree  $\lambda(G)$  are justified sequences of nodes defined by induction over the rules given in Table II.

A remark on rule (Lam). If  $n'$  is a variable then it should point to the binder. But there may be several occurrences of the binder, and this is what the P-view is for: it selects an occurrence via the pointer from the variable in question.

**Remark 6.3.** The pointers in a traversal over any computation tree  $\lambda(G)$  are uniquely reconstructible from the underlying sequence of nodes and their respective labels; thus pointers are not an additional structure imposed on the underlying sequence. However it is convenient (e.g. in the definition of P-view below) to define traversals as sequences equipped with pointers. Another advantage of pointers is that they help to clarify the correspondence between traversals and interaction sequences (that arise in the construction of the game semantics of the recursion scheme in question).

Note that the only rule in Table II that can lead to extending a traversal in a non unique way is the rule (Sig) that allows  $ar(f)$  possible extensions. Traversals define an infinite

<sup>9</sup>Justified sequences were first introduced to represent plays in dialogue games between two players, O and P [Hyland and Ong 2000]. A play is a certain sequence of alternating O-moves and P-moves. A player may only make a given move  $m$  provided the move that enables it,  $m'$  (say), has already been played; and if so, this situation is represented by a pointer from  $m$  to  $m'$ .

rooted deterministic digraph<sup>10</sup>

$$Tr(G) = \langle V, E \subseteq V \times (\text{Dir}(\Sigma) \cup \{\varepsilon\}) \times V, l : V \rightarrow (\Sigma \cup \{\#\}), v_0 \rangle$$

where

- $V$  is the set of all traversals over  $\lambda(G)$ .
- $(v, \varepsilon, v') \in E$  iff  $v'$  is obtained from  $v$  by applying one of the following rules: (App), (Var) or (Lam).
- $(v, i, v') \in E$  iff  $v'$  is obtained from  $v$  by applying rule (Sig) with parameter  $i$ .
- $l(v) = \lambda(G)(n)$  if  $v$  ends by a node  $n$  labelled by a terminal, and  $l(v) = \#$  otherwise.
- The root  $v_0$  is the traversal  $\varepsilon$ .

Note that  $Tr(G)$  is a deterministic tree. By taking its  $\varepsilon$ -closure as explained below, we obtain a  $\Sigma$ -labelled ranked tree. More precisely, the traversal tree of  $G$ , denoted  $TrTree(G)$ , is obtained as follows from  $Tr(G)$ . For every  $i \in \text{Dir}(\Sigma)$ , add an  $i$ -labelled edge from  $v_1$  to  $v_2$  whenever there is a path from  $v_1$  to  $v_2$  labelled by a word that matches  $i\varepsilon^*$  (note that we treat  $\varepsilon$  as a standard letter), and there is no outgoing  $\varepsilon$ -labelled edge from  $v_2$ ; for every  $i \in \text{Dir}(\Sigma)$ , whenever there is an edge from  $v_1$  to a node  $v_2$  from which there is an infinite path made only of  $\varepsilon$ -labelled edges, create a new vertex  $v_1^{i,\perp}$  labelled by  $\perp$  and add an  $i$ -labelled edge from  $v_1$  to  $v_1^{i,\perp}$ ; then remove any vertex that is the source of an  $\varepsilon$ -labelled edge and remove any  $\varepsilon$ -labelled edge. In case the resulting object is empty, simply replace it by a tree made only of a root labelled by  $\perp$ . Note that the resulting object is always a deterministic,  $\varepsilon$ -free,  $\Sigma$ -labelled tree (indeed,  $\#$ -labelled nodes, i.e. those that are not labelled by terminal, as well as  $\varepsilon$ -labelled edges have all been removed). Define the root as the (unique) node that is reachable in  $Tr(G)$  by a (possibly empty) sequence of  $\varepsilon$ -labelled edges, and not the source of an  $\varepsilon$ -labelled edge. Call  $TrTree(G)$  the resulting tree.

In the sequel, to stick to our original definition of ranked-trees, we regard  $TrTree(G)$  as the  $\Sigma$ -labelled ranked tree whose domain is the set of words in  $\text{Dir}(\Sigma)$  that labels finite path from the root, and where a node  $n$  is labelled by  $l(v)$  where  $v$  is the (unique) node reached by following from the root the path labelled by  $n$ .

It turns out that the value tree and the traversal tree are equal [Ong 2015b]:

Theorem 6.4 (Correspondence Theorem). For every recursion scheme  $G$ , we have  $\llbracket G \rrbracket = TrTree(G)$ .

We finally define the P-view of a justified sequence. The P-view  $\ulcorner t \urcorner$  of a justified sequence  $t$  is a subsequence defined by recursion as follows:

- $\ulcorner \lambda \urcorner = \lambda$  for a dummy lambda  $\lambda$ .
- $\ulcorner t n' \urcorner \dots n \urcorner = \ulcorner t \urcorner n' \urcorner n$  whenever  $n$  is a lambda node (hence  $n'$  is a non-lambda node). Node  $n'$  is either  $@$  or a signature symbol, and in the latter case the lambda is dummy.
- $\ulcorner t n \urcorner = \ulcorner t \urcorner n$  whenever  $n$  is a non-lambda node.

In the second clause above, if in  $t n' \dots n$  the non-lambda node  $n'$  has a pointer to some node-occurrence  $l$  (say) in  $t$ , and if  $l$  appears in  $\ulcorner t \urcorner$ , then in  $\ulcorner t \urcorner n' \urcorner n$  the node  $n'$  is defined to point to  $l$ ; otherwise  $n'$  has no pointer. Similarly, in the third clause above, if in  $t n$  the non-lambda node  $n$  has a pointer to some node-occurrence  $l$  (say) in  $t$  and if  $l$  appears in  $\ulcorner t \urcorner$ , then in  $\ulcorner t \urcorner n$  the node  $n$  is defined to point to  $l$ ; otherwise  $n$  has no pointer.

<sup>10</sup>In fact we have a small abuse of terminology and notation here as in our original definition of a DDG we do not allow  $\varepsilon$ -labelled edges which we do here only for those edges outgoing an  $\#$ -labelled vertex. For ease of writing we also make the edge label explicit by describing edges as elements of  $V \times \{\text{Dir}(\Sigma) \cup \{\varepsilon\}\} \times V$ .

It is easy to see that the P-view of a justified sequence is always lambda/non-lambda alternating, that may not necessarily satisfy the pointer condition. When applied to traversals, the P-view has some nice properties.

Proposition 6.5. [Ong 2006a, Lemma 2][Ong 2006b, Proposition 6] Let  $t$  be a finite traversal over a computation tree  $\lambda(G)$ . Then the following hold.

- $\ulcorner t \urcorner$  is a well-defined justified sequence.
- $\ulcorner t \urcorner$  is a path in the computation tree  $\lambda(G)$  from the root to the last node in  $t$ .

Traversals (and related concepts) were defined with respect to the computation tree  $\lambda(G)$ . As  $\lambda(G)$  is obtained by unfolding the HORS graph  $\text{Gr}(G)$  we can associate with every sequence of nodes in  $\lambda(G)$  a unique sequence of vertices in  $\text{Gr}(G)$ . This mapping, when restricted to (the sequences of nodes underlying) traversals over the computation tree  $\lambda(G)$ , is injective. Hence, depending on the context, we may see traversals either as sequences of nodes in  $\lambda(G)$  or as sequences of vertices in  $\text{Gr}(G)$  (in this case, it is easy to reconstruct the corresponding traversal over  $\lambda(G)$ ). Note that traversals over  $\text{Gr}(G)$  could equivalently be defined by stating the rules in Table II in the framework of  $\text{Gr}(G)$ .

## 6.2. CPDA( $G$ ) — the CPDA determined by a recursion scheme $G$

As stated in the previous section (Correspondence Theorem), traversals provide an alternative way to describe the value tree of a given scheme  $G$ . We will now define a CPDA,  $\text{CPDA}(G)$ , and will show that its transition graph  $\text{Graph}(\text{CPDA}(G))$  is trace-equivalent with  $\text{Tr}(G)$ . As a byproduct, unfolding the  $\varepsilon$ -closure of  $\text{Graph}(\text{CPDA}(G))$  leads to the same tree as  $\text{TrTree}(G) = \lambda(G)$ , equivalently  $\text{CPDA}(G)$  generates the same tree as  $G$ .

Fix an order- $n$  recursion scheme  $G$  and the HORS graph

$$\text{Gr}(G) = \langle V, E \subseteq V \times V, \lambda_G : V \rightarrow \Lambda_G, v_0 \in V \rangle$$

determined by it. Note that  $G$  is not assumed to be homogeneously typed, and hence, not necessarily safe

Remark 6.6. For convenience, in the definition of the transform  $\text{CPDA}(G)$ , we shall write  $\text{push}_1^{a,1}$  as  $\text{push}_1^a$ , effectively ignoring the 1-link (to the preceding stack symbol). This is harmless since 1-links are guaranteed not to feature in any of collapse operations of the transform  $\text{CPDA}(G)$ .

Definition 6.7. The transform  $\text{CPDA}(G)$  is an  $n$ -CPDA with a single dummy control state (that we omit from now for simplicity) that has the set  $V$  of nodes as the stack alphabet. The initial configuration is the  $n$ -stack  $[\dots [\perp v_0] \dots]$  i.e.  $\text{push}_1^{v_0} \perp_n$ , where  $v_0$  is the root of  $\text{Gr}(G)$ . Let  $u$  range over the stack symbols of  $\text{CPDA}(G)$ . For ease of explanation, we define the transition map  $\delta$  as a function that takes a node  $u \in V$  to a sequence of stack operations (in particular, this allows us to have a single control state), by a case analysis of the label (from  $\Lambda_G$ ) of  $u$ . The definition is presented in Table III.

Remark 6.8. The transformation is radically different from the compilation method of Knapik et al. [Knapik et al. 2002; Knapik et al. 2005]. To date, it is not known whether the approach in [Knapik et al. 2005] is extendable to non-homogeneously typed recursion schemes of order 2. More generally, it is not known whether the method is extendable to arbitrary recursion schemes of all finite orders.

## 6.3. $\text{Graph}(\text{CPDA}(G))$ and $\text{Tr}(G)$ are trace equivalent

We first recall the standard notion of trace equivalence, that we state here in the specific case of labelled rooted deterministic digraphs. Let  $\mathcal{K}_1 = \langle V_1, E_1 \subseteq V_1 \times \Pi \times V_1, l : V_1 \rightarrow \Lambda, r_1 \rangle$  and  $\mathcal{K}_2 = \langle V_2, E_2 \subseteq V_2 \times \Pi \times V_2, l : V_2 \rightarrow \Lambda, r_1 \rangle$  be two graphs with the same alphabet

Table III. Definition of the transform  $\text{CPDA}(G)$ .

If  $u$ 's label is not a variable, the action is just a  $push_1^v$ , where  $v$  is an appropriate child of the node  $u$ . Precisely:

- (A). If the label is an @ then  $\delta(u, \varepsilon) = push_1^{E_0(u)}$ .
- (S). If the label is a  $\Sigma$ -symbol  $f$  then  $\delta(u, i) = push_1^{E_i(u)}$ , for every  $1 \leq i \leq ar(f)$ . Note that if  $f$  is nullary, the automaton terminates.
- (L). If the label is a lambda then  $\delta(u, \varepsilon) = push_1^{E_1(u)}$ .

Suppose  $u$  is labelled by a variable which is the  $i$ -parameter of the lambda node  $binder(u)$ ; and suppose  $binder(u)$  is a  $j$ -child. Let  $p$  be the span of the variable node  $u$ .

- (V<sub>1</sub>). If the variable has order  $l \geq 1$ , then

$$\delta(u, \varepsilon) = \begin{cases} push_{n-l+1}; pop_1^{p+1}; push_1^{E_i(top_1), n-l+1} & \text{if } j = 0 \\ push_{n-l+1}; pop_1^p; collapse; push_1^{E_i(top_1), n-l+1} & \text{otherwise} \end{cases}$$

where  $pop_1^p$  means the operation  $pop_1$  iterated  $p$  times, and  $push_1^{E_i(top_1), k}$  is defined to be the operation  $s \mapsto push_1^{E_i(top_1 s), k} s$ .

- (V<sub>0</sub>). Otherwise (i.e. the variable has order 0)

$$\delta(u, \varepsilon) = \begin{cases} pop_1^{p+1}; push_1^{E_i(top_1)} & \text{if } j = 0 \\ pop_1^p; collapse; push_1^{E_i(top_1)} & \text{otherwise.} \end{cases}$$

for labelling vertices (resp. edges). We say that  $\mathcal{K}_1$  and  $\mathcal{K}_2$  are trace-equivalent just if for every  $x \in \{1, 2\}$ , for every path in  $\mathcal{K}_x$  that starts from the root  $r_x$ , there is a (unique) corresponding path in  $\mathcal{K}_{\bar{x}}$  (here  $\bar{x} = 2$  if  $x = 1$  and  $\bar{x} = 1$  otherwise) with the same label in  $\Pi^*$ . Moreover the terminal vertices of both paths are labelled by the same element in  $\Lambda$ .

Note that the previous notion does not treat the silent letter  $\varepsilon$  in a specific way (like one would do for weak bisimulation): it is considered as a standard letter.

The following proposition follows by definition.

**Proposition 6.9.** Let  $\mathcal{K}_1$  and  $\mathcal{K}_2$  be two trace-equivalent labelled rooted deterministic digraphs (possibly with  $\varepsilon$ -labelled edges). Then the trees obtained by unfolding the  $\varepsilon$ -closure of those two digraphs are the same.

Let  $\text{Graph}_\ell(\text{CPDA}(G))$  be the vertex labelled version of  $\text{Graph}(\text{CPDA}(G))$  (with the initial configuration as its root) that is obtained by labelling every vertex with the label of the topmost symbol of the corresponding stack if it belongs to  $\Sigma$  and by  $\sharp$  otherwise. We have the following key result (to be proved later).

**Theorem 6.10.** For every order- $n$  recursion scheme  $G$ ,  $\text{Graph}_\ell(\text{CPDA}(G))$  and  $\text{Tr}(G)$  are trace-equivalent.

An important consequence of Theorem 6.10 is the following.

**Theorem 6.11 (Equi-Expressivity 2).** For every order- $n$  recursion scheme  $G$ ,  $\text{CPDA}(G)$  (together with the identity labelling function) generates (the same tree as) the value tree  $\llbracket G \rrbracket$ .

**Proof.** Take an order- $n$  recursion scheme  $G$ . Using Theorem 6.10,  $\text{Graph}_\ell(\text{CPDA}(G))$  and  $\text{Tr}(G)$  are trace-equivalent. By definition, the tree generated by  $\text{CPDA}(G)$  and the identity function, coincides with that obtained by unfolding the  $\varepsilon$ -closure of  $\text{Graph}_\ell(\text{CPDA}(G))$ . Hence, using Proposition 6.9, this tree coincides with that obtained by unfolding the  $\varepsilon$ -

closure of  $Tr(G)$ , which is  $TrTree(G)$ . As the latter coincides with the value tree  $\llbracket G \rrbracket$  (Theorem 6.4), it concludes the proof.  $\square$

#### 6.4. Proof of Theorem 6.10

We now turn to the technical core of this section. Fix an order- $n$  recursion scheme  $G$ . In order to prove that  $\text{Graph}_\ell(\text{CPDA}(G))$  and  $Tr(G)$  are trace-equivalent, it suffices to establish the following.

Property 6.12. Suppose

$$s_1 \xrightarrow{x_1} s_2 \xrightarrow{x_2} s_3 \xrightarrow{x_3} \cdots \xrightarrow{x_{m-1}} s_m.$$

is a path in  $\text{Graph}_\ell(\text{CPDA}(G))$  starting from the root and

$$t_1 \xrightarrow{x_1} t_2 \xrightarrow{x_2} t_3 \xrightarrow{x_3} \cdots \xrightarrow{x_{m-1}} t_m$$

is a path in  $Tr(G)$  starting from the root (i.e. the trivial traversal  $\varepsilon$ ). Suppose  $s_m, t_m$  have the same label from  $\Sigma \cup \{\#\}$ . Then neither path can be extended or both can be extended in the same way:

- by a unique  $\varepsilon$ -labelled edge (if the final vertices of the paths are labelled by  $\#$ ) or
- by  $ar(f)$  edges with labels in  $\{1, \dots, ar(f)\}$  (if the final vertices of the paths are labelled by  $f \in \Sigma$ ).

Moreover, if each path is extended by an edge with the same label, the resulting paths end in vertices with the same label.

In order to prove the above, we spell out in detail how both paths are related. First, we shall see that, for each  $1 \leq i \leq n$ , the sequence of node labels corresponding to the top 1-stack, written  $\lambda_G(\text{top}_2(s_i))$ , is the P-view of  $t_i$ . i.e.

$$\lambda_G(\text{top}_2(s_i)) = \lceil t_i \rceil.$$

Secondly we construct a kind of approximant of  $t_i$ , written  $\widehat{t}_i$ , which is obtained from  $t_i$  by removing all segments  $w$  sandwiched between matching pairs of the shape

$$\begin{array}{c} \text{\textcircled{ $i$ }} \\ \text{\$} \quad \text{\textit{w}} \quad \text{\lambda} \end{array}$$

where  $\text{\$}$  is either an order-1 variable or an  $\text{\textcircled{ $\lambda$ }}$ -symbol, and  $i \geq 1$ , and we do it from right to left. Note that by definition of traversal, the segment  $w$  necessarily has the shape

$$\begin{array}{c} \text{\textcircled{ $i$ }} \\ \text{\lambda}\overline{\varphi} \quad \cdots \quad x \end{array}$$

where  $x$  is an order-0 variable symbol and  $\overline{\varphi}$  is a list of variables in which  $x$  occurs. Finally, we remove all pointers from  $\widehat{t}_i$ . See Example 6.18 for an illustration of this construction. We then transform each  $n$ -stack  $s_i$  to a sequence of nodes  $\underline{s}_i$ , which will be shown to coincide with  $\widehat{t}_i$ .

Remark 6.13. Note that  $\text{CPDA}(G)$  handles variables of order 0 differently from those at higher orders. One could have treated level 0 in the same way to obtain correspondence with  $t$  (rather than  $\widehat{t}$ ), but this would cost an extra stack level.

In order to construct the sequence  $\underline{s}_i$  from an  $n$ -stack  $s_i$ , we follow a simple recipe.

- (1) We “flatten” the  $n$ -stack  $s_i$  so that it has the form of a well-bracketed sequence such as the following (top of stack is the right-hand end)

$$\llbracket \llbracket \cdots \rrbracket \cdots \llbracket \cdots \rrbracket \rrbracket \llbracket \llbracket \cdots \rrbracket \rrbracket \cdots \llbracket \llbracket \cdots \rrbracket \llbracket \cdots \rrbracket \rrbracket$$



- (2) The target of any pointer to a stack is deemed to be the rightmost symbol representing the stack, i.e. it is always an occurrence of  $\rfloor$ .
- (3) The required subsequence – which we shall write as  $s_{\underline{s}}$  – is obtained by a right-to-left scan of the well-bracketed sequence above according to the following rules.
  - When an occurrence of  $\rfloor$  is encountered, we simply continue the scan without recording  $\rfloor$ .
  - We record any stack symbols that are being scanned.
  - Whenever we encounter the source of a link of order 2 or more, the scan jumps to its target (an occurrence of  $\rfloor$ ) without recording any nodes sandwiched in-between. The source of the link is always recorded.
  - The scan ends as soon as some  $\lceil$  is hit.

Note that the last condition is necessary to ensure that  $s_{\underline{s}}$  is suitably defined for every prefix of a reachable stack. This will be important in the proof of Property 6.17.

Here is a more formal definition.

Definition 6.14. Let  $s$  be an  $n$ -stack. The sequence  $s_{\underline{s}}$  of stack symbols is defined as follows.

$$s_{\underline{s}} = \begin{cases} \varepsilon & \text{top}_2(s) = \lceil \rfloor, \\ \underline{\text{pop}_1(s)} \lambda_G(u) & \text{top}_1(s) = u \text{ and } u \text{ has a 1-link,} \\ \underline{\text{collapse}(s)} \lambda_G(u) & \text{top}_1(s) = u \text{ and } u \text{ has a } k\text{-link with } k > 1. \end{cases}$$

The next definition relates configurations of  $\text{CPDA}(G)$  with traversals over  $\lambda(G)$ .

Definition 6.15. Let  $G$  be an order- $n$  recursion scheme, let  $s$  be a reachable configuration of  $\text{CPDA}(G)$ , and let  $t$  be a traversal over  $\lambda(G)$ . We shall say that  $s$  computes  $t$  if and only if the following conditions hold.

- (a)  $\lambda_G(\text{top}_2(s)) = \ulcorner t \urcorner$ .
- (b)  $s_{\underline{s}} = \hat{t}$ .
- (c) Suppose  $\text{top}_2(s) = [v_1, \dots, v_n]$ . Let  $v'_1, \dots, v'_n$  be the respective occurrences of  $v_1, \dots, v_n$  in  $t$  that contribute to  $\ulcorner t \urcorner$ . Then  $\text{pop}_1^{n-i}(s)$  computes  $t_{\leq v'_i}$  for every  $1 \leq i < n$ .
- (d) Using the same notation as in (c), suppose  $v_i$  has a link to an  $l$ -stack  $\sigma$ . Let  $s_{\sigma}$  be the prefix of  $s$  such that  $\sigma$  is its top  $l$ -stack, i.e.  $s_{\sigma} = \text{collapse}(\text{pop}_1^{n-i}(s))$ . Then  $s_{\sigma}$  computes  $t_{< v'_i}$ .

Note that the definition is not circular, since  $t_{\leq v'_i}$  ( $1 \leq i < n$ ) and  $t_{< v'_i}$  ( $1 \leq i \leq n$ ) are strictly shorter than  $t$ . In what follows we shall blur the distinction between  $v_i$  and its occurrence  $v'_i$ , as it will be clear from the context which occurrence is meant. The notion of computing traversals is stable under higher-order pushes, as specified in the next lemma.

Lemma 6.16. Let  $s$  be an  $n$ -stack,  $s' = \text{push}_k(s)$  and  $k \geq 2$ . Given an  $n$ -stack  $s''$  such that  $s < s'' \leq s'$ , let  $s_{s''}$  be the prefix of  $s$  obtained after the same sequence of pop-operations as that used to obtain  $s''$  from  $s'$ . Then  $s''$  computes  $t$  if and only if  $s_{s''}$  computes  $t$  for any traversal  $t$ .

Proof. By induction on the length of  $s''$ . The base case of  $s''$  is trivial: we have  $\text{top}_{k+1}(s'') = \perp_k = \text{top}_{k+1}(s_{s''})$  and the empty traversal is computed in both cases.

For the inductive step, we need to show that  $s''$  and  $s_{s''}$  compute the same traversals. To that end, observe that  $\text{top}_2(s'') = \text{top}_2(s_{s''})$ , because – according to the definition of  $\text{push}_k - \text{top}_2(s'')$  is a clone of  $\text{top}_2(s_{s''})$ . Note also that  $\text{push}_k$  preserves links. Hence,  $\underline{s''} = \underline{s_{s''}}$ .

Thus, conditions (a) and (b) are fulfilled for the same traversal. For (c) and (d), recall that  $\text{top}_2(s'') = \text{top}_2(s_{s''})$  and observe that the requirements regarding computability of traversals by  $\text{pop}_1^{n-i}$  (for (c)) and  $s''_\sigma$  (for (d)) are covered by the inductive hypothesis.  $\square$

The following implies Property 6.12.

Property 6.17. Suppose

$$s_1 \xrightarrow{x_1} s_2 \xrightarrow{x_2} s_3 \xrightarrow{x_3} \dots \xrightarrow{x_{m-1}} s_m$$

is a path in  $\text{Graph}_\ell(\text{CPDA}(G))$  starting from the root and

$$t_1 \xrightarrow{x_1} t_2 \xrightarrow{x_2} t_3 \xrightarrow{x_3} \dots \xrightarrow{x_{m-1}} t_m$$

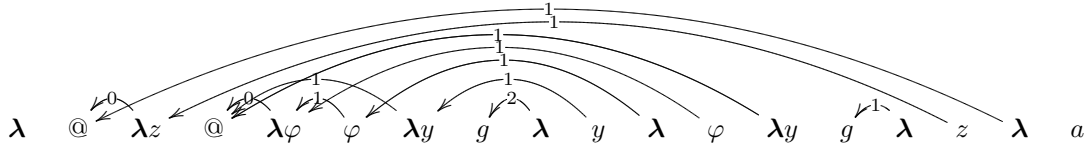
is a path in  $\text{Tr}(G)$  starting from the root (i.e. the trivial traversal  $\varepsilon$ ). If  $s_m$  and  $t_m$  are labelled by the same element of  $\Sigma \cup \{\#\}$ , then the following conditions hold.

- (i) Let  $u = \text{top}_1(s_m)$ . Then  $u$  has a link in  $s_m$  if and only if it is a  $j$ -child ( $j > 0$ ) labelled by a lambda of type  $A^{11}$  which has order  $l \geq 1$ . Further, if  $u$  has a link, it points to an  $(n-l)$ -stack.
- (ii)  $s_i$  computes  $t_i$  for all  $1 \leq i \leq m$ .

Note that (ii) implies that  $\text{top}_1(s_m)$  is the same as the final vertex in  $t_m$ . Consequently, the respective definitions of traversals and CPDA( $G$ ) imply Property 6.12.

Before going to the proof, we should give some examples that illustrate the relationship between paths in CPDA( $G$ ) and in  $\text{Tr}(G)$ .

Example 6.18. Take the following traversal over the computation tree of  $G$  (recall that variable  $z$  has order 0 and variable  $\varphi$  has order 1) in Example 6.1 (see also Figure 2 for the associated HORS graph):



In Figure 3 we give the path of the corresponding 2-CPDA that ends in a configuration that computes the above traversal. For ease of reading, in Figure 3 (and later in Figures 6 and 7), instead of stack symbols we shall write their image by  $\Lambda_G$  rather than the exact symbol from  $V$ .

To save space, we only present the interesting configurations in which the  $\text{top}_1$ -element of the stack is a variable node. In the picture, the top of a stack is at the right-hand end, and links are represented by dotted arrows. Set  $t$  to be the prefix of the above traversal that ends in the node labelled by  $z$ . We have

$$\hat{t} = \lambda \quad @ \quad \lambda z \quad @ \quad \lambda \varphi \quad \varphi \quad \lambda \quad \varphi \quad \lambda y \quad g \quad \lambda \quad z$$

which coincides with the 2-stack  $\underline{s}$  ( $s$  is marked in Figure 3) by following the recipe.

Example 6.19. Consider the order-3 HORS graph in Figure 5 where variables  $x_1, x_2, z$  have order 0, variable  $\varphi$  has order 1 and variable  $\Psi$  has order 2. For ease of reference, we give nodes numeric names, which are indicated (within square-brackets) as superscripts. Take the traversal  $\mathbf{t}$  in Figure 4.

<sup>11</sup>We are abusing notation here. Technically,  $\lambda^{\bar{\psi}}$  is a terminal symbol from the long transform  $\bar{G}$ , hence it should have order 0 or 1. However, by the type of  $\lambda^{\bar{\psi}}$  we mean  $(\text{type}(\psi_1), \dots, \text{type}(\psi_k), o)$ , where  $\bar{\psi} = \psi_1 \dots \psi_k$ .

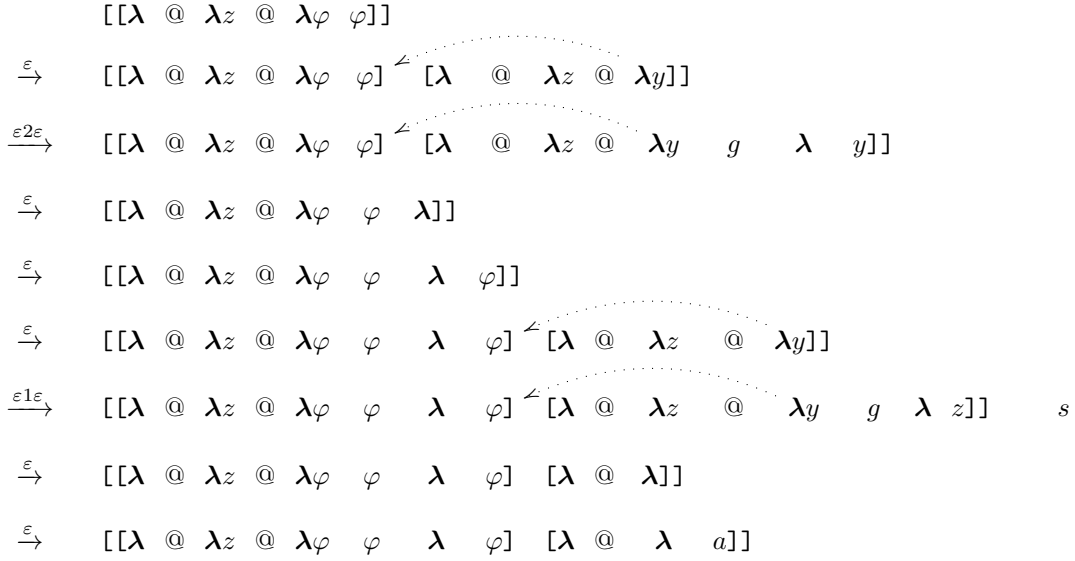


Fig. 3. A run of a 2-CPDA

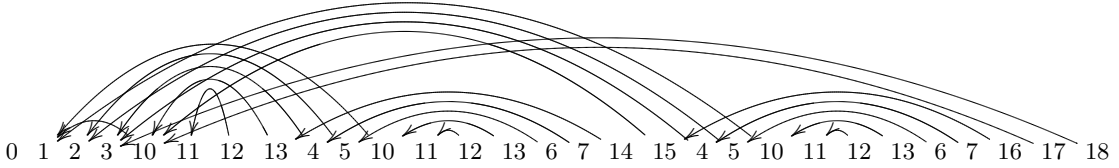


Fig. 4. An order-3 traversal

We present a run of the 3-CPDA that computes the traversal  $\mathbf{t}$  in Figure 6 followed by Figure 7 (for ease of reading, we represent nodes by their labels).

To see the correspondence with the traversal  $\mathbf{t}$ , note that configurations  $s_2$  and  $s_3$  in Figures 6 and 7 respectively have the same  $top_1$ -element which is node 7 (labelled by  $x_1$ ). They correspond respectively to the two prefixes of  $\mathbf{t}$  that end in node 7.

The traversal  $t$  corresponding to  $s_3$  is the prefix of  $\mathbf{t}$  that ends in the later occurrence of 7; we have

$$\hat{t} = \lambda @ \lambda \Psi \Psi \lambda \varphi z f \lambda \varphi \lambda \varphi \lambda x_1 x_2 \Psi \lambda \varphi z f \lambda \varphi \lambda x'_1 x'_2 x_1$$

The reader might wish to check that  $\underline{s}_3 = \hat{t}$ . (Note that the justification pointers are uniquely reconstructible from the underlying sequence of nodes and their respective labels.)

The rest of the section is devoted to the proof of Property 6.17. The proof is by induction on  $m$ . Clearly the above assertions are valid when  $m = 1$ . For the inductive case, we assume that the property holds for some  $m \geq 1$ .

We shall do so by a case analysis of the label of  $top_1(s_m) = u$ .

First suppose  $u$ 's label is not a variable. Then  $s_{m+1} = push_1^v(s_m)$ , for an appropriate node. In particular, no new link is created.

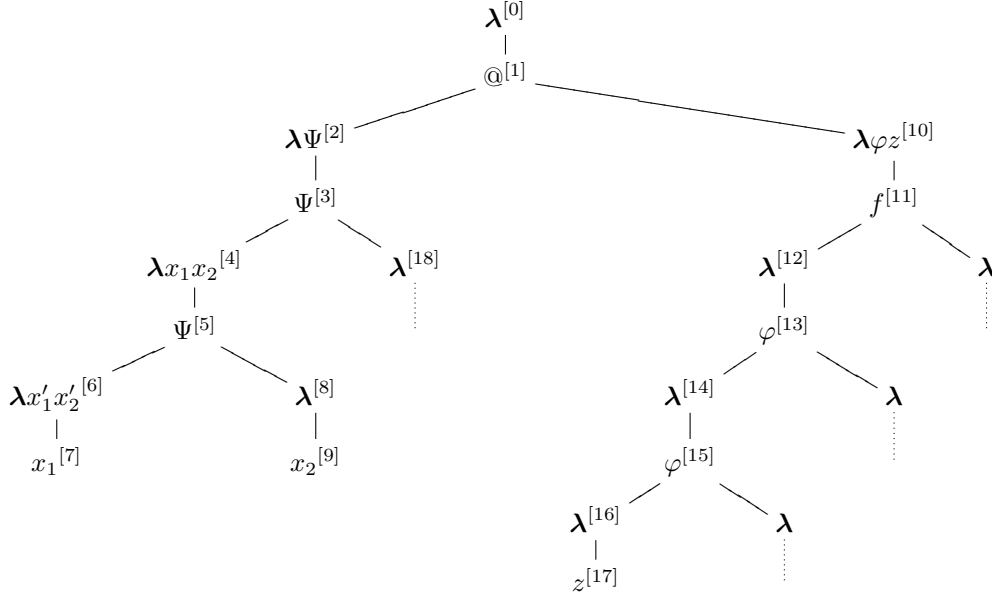


Fig. 5. An example of an order-3 HORS graph.

For (i), observe that, because  $u$ 's label is not a variable, it follows from the definition of  $\text{CPDA}(G)$  that, if  $v$  was a  $j$ -child labelled by a lambda of type  $A$ , then  $u$  would have to be labelled by a  $\Sigma$ -symbol and, thus, the order of  $A$  would be 0.

For (ii),  $t_m \xrightarrow{x_m} t_{m+1}$  implies that  $t_{m+1} = t_m v$ , where  $v$  has a pointer to a suitable node (there is only one way in which a pointer from  $v$  can be inserted so as to make  $t_{m+1}$  into a traversal). We shall show that  $s_{m+1}$  computes  $t_{m+1}$ .

For (a), we need to check that  $\lambda_G(\text{top}_2(s_{m+1})) = \ulcorner t_{m+1} \urcorner$ . We have  $\lambda_G(\text{top}_2(s_{m+1})) = \lambda_G(\text{top}_2(s_m))v$  and, in all three cases corresponding to the rules **(A)**, **(S)**, **(L)**,  $\ulcorner t_{m+1} \urcorner = \ulcorner t_m \urcorner v$  holds. Thus, by induction hypothesis, we get  $\lambda_G(\text{top}_2(s_{m+1})) = \ulcorner t_{m+1} \urcorner$ . For (b), we note that  $\underline{s}_{m+1} = \underline{s}_m v$  and  $\widehat{t}_{m+1} = \widehat{t}_m v$ . So, by induction hypothesis,  $\underline{s}_{m+1} = \widehat{t}_{m+1}$ . Condition (c) follows immediately from the induction hypothesis and, because no new links have been created, so does (d).

Next suppose  $u$ 's label is an order- $l$  variable, which is the  $i$ -parameter of  $\text{binder}(u)$  (note that then we have  $i \geq 1$ ) and suppose  $\text{binder}(u)$  is a  $j$ -child. Then  $s_{m+1} = \delta(u)(s_m)$  where  $\delta(u)$  is given in Definition 6.7. There are four cases; in the following we shall use the notations from Definition 6.7. In order to simplify the notations, we should refer to  $s_m$  (resp.  $t_m$ ) as  $s$  (resp.  $t$ ) and to  $s_{m+1}$  (resp.  $t_{m+1}$ ) as  $s'$  (resp.  $t'$ ).

1. Case  $l \geq 1$  and  $j = 0$ . Let  $\varphi_i$  be the order- $l$  variable labelling  $u$ .

By the induction hypothesis of (ii),  $u$  must be the last node of  $t$ . It then follows from the definition of a traversal (and from  $\text{binder}(u)$  being a 0-child) that  $t$  has the following shape:

$$t = \cdots \underset{\textcircled{\lambda\varphi}}{u_0} \overset{0}{\curvearrowright} \underset{\varphi_i}{u_1} \cdots \overset{i}{\curvearrowleft} u$$

(in the figure, the label of a node is the symbol just below it). Since the P-view of a traversal satisfies the pointer condition and is a path in the HORS graph (Proposition 6.5),  $\ulcorner t \urcorner$  has

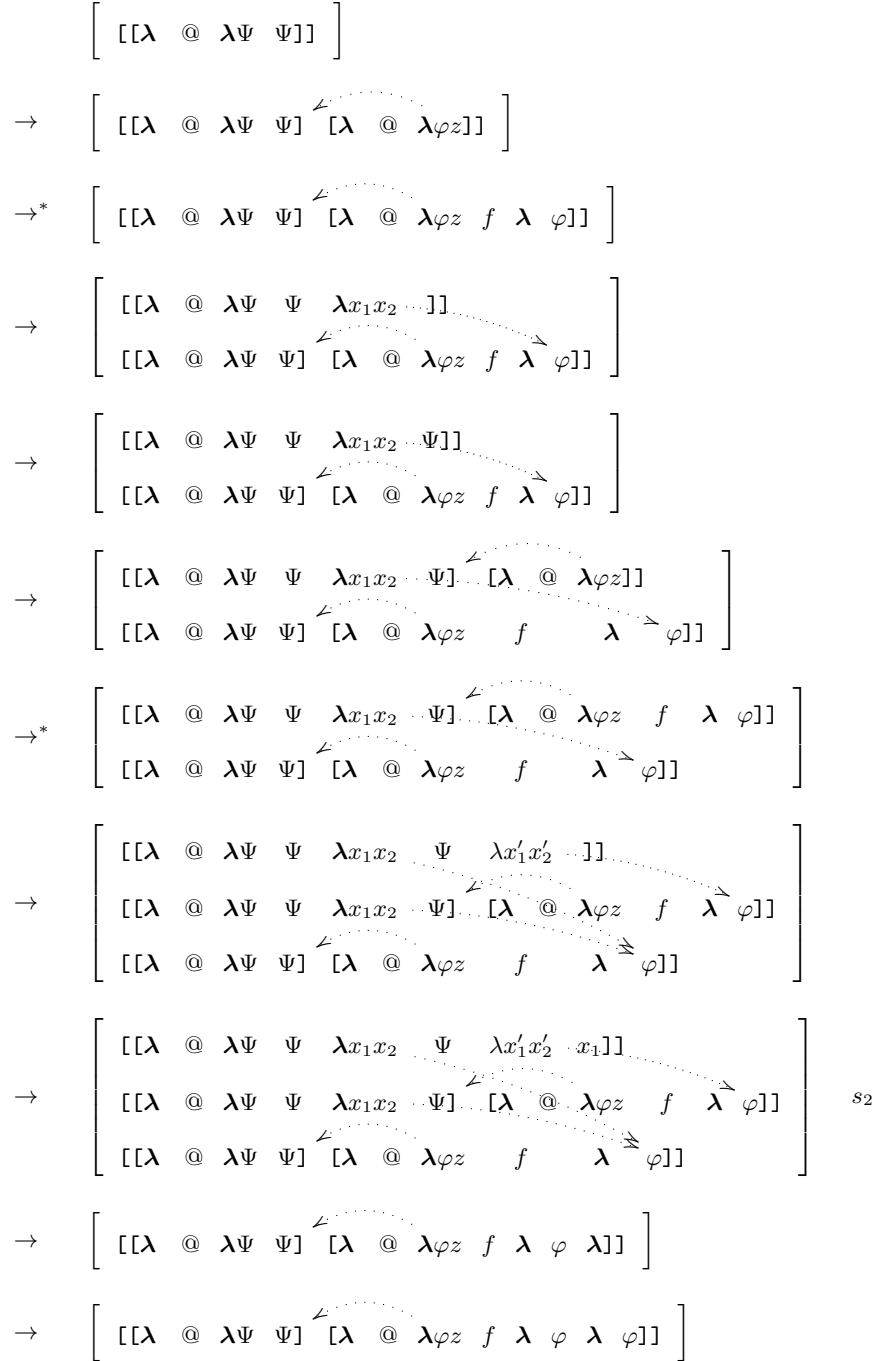
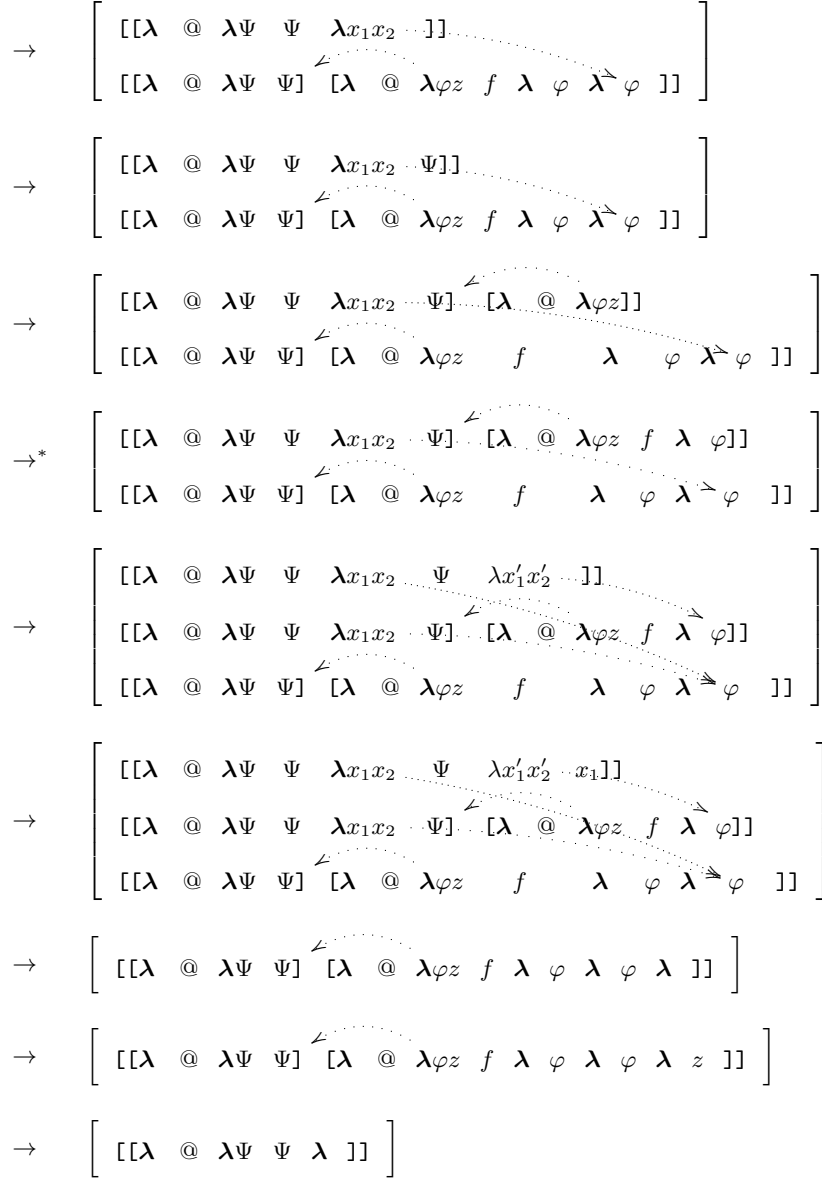


Fig. 6. A run of a 3-CPDA (Part 1 of 2).



83

Fig. 7. A run of a 3-CPDA (Part 2 of 2).

the shape  $\dots u_0 \underbrace{u_1 \dots u}_\theta$  and the segment  $\theta$  has length  $p + 1$ , where  $p$  is the span of the variable node  $u$ . Indeed  $u$  belongs to  $\ulcorner t \urcorner$  and so does  $u_1$  thanks to the pointer condition; the length of  $\theta$  comes from  $\ulcorner t \urcorner$  being a path.

Consider the operation  $\delta(u) = \text{push}_{n-l+1}; \text{pop}_1^{p+1}; \text{push}_1^{E_i(\text{top}_1), n-l+1}$ . By the induction hypothesis of (ii), the top 1-stack of  $s$  — call it  $\sigma$  — is the P-view of  $t$ . Since the top 1-stack of  $\text{push}_{n-l+1}s$  is a copy of  $\sigma$ , applying  $\text{pop}_1^{p+1}$  to  $\text{push}_{n-l+1}(s)$  returns a stack that has

the @-labelled node  $u_0$  as the  $top_1$ -element. The node that is pushed onto the top of the stack at this point is the  $i$ -child of  $u_0$ , which we call  $v$ . Further, it has a link to the top  $(n-l)$ -stack of the prefix  $s$  of  $s'$ , hence  $collapse(s') = s$ .

It follows from the structure of  $\lambda(G)$  that  $v$  must be labelled by  $\lambda\bar{\psi}$  (say) of the same type as the label  $\varphi_i$  of  $u$ , i.e. its type is also of order  $l \geq 1$ . Thus, since  $i \geq 1$ , (i) follows as required.

For (ii), observe that  $t' = tv$ , where  $v$  has a pointer (labelled by  $i$ ) to the occurrence of  $u_0$  indicated in the figure above. Also, we have  $t \xrightarrow{\varepsilon} t'$ . We shall show that  $s'$  computes  $t'$ .

- (a) We need to show  $\lambda_G(top_2(s')) = \ulcorner t' \urcorner$ . By definition of  $s'$ , we have  $\lambda_G(top_2(s')) = \lambda_G(top_2(s)_{\leq u_0}v)$ , i.e.  $top_2(s')$  is the prefix of the 1-stack  $top_2(s)$  — regarded as a sequence — up to and including the occurrence of  $u_0$  described above, extended by  $v$ . By induction hypothesis (a), we have  $\lambda_G(top_2(s)) = \ulcorner t \urcorner$ . Thus

$$\lambda_G(top_2(s')) = \ulcorner t \urcorner_{\leq u_0} v = \ulcorner t_{\leq u_0} \urcorner v = \ulcorner t' \urcorner$$

as required (the second equation holds because  $u_0$  appears in  $\ulcorner t \urcorner$ ).

- (b) We have  $\underline{s}' = \underline{s}v$  (indeed  $v$  has a link and  $collapse\ s' = s$ ) and  $\widehat{t}' = \widehat{t}v$ . Since  $\underline{s} = \widehat{t}$  by induction hypothesis (b), we have  $\underline{s}' = \widehat{t}'$ .
- (c) Because the top 1-stack of  $s'$  is (a copy of) a prefix of  $top_2(s)$  extended with  $v$ , we can simply appeal to the induction hypothesis (ii) — namely, part (c) and the fact that  $s$  computes  $t$ .
- (d) For the same reason as above, (d) holds for all links in  $top_2(s')$  except (possibly) the single new link. Let  $\sigma'$  be the  $(n-l)$ -stack pointed at from  $v$ . Then we have  $s'_{\sigma'} = s$ . Because  $t = t'_{<v}$  and  $s$  computes  $t$ , (d) also holds for the new link.

2. Case  $l \geq 1$  and  $j > 0$ . Suppose the label of  $u$  is the order- $l$  variable  $\varphi_i$ , which is the  $i$ th item of the list  $\bar{\varphi}$ .

By induction hypothesis (ii) and definition of a traversal,  $t$  has the following shape:

$$t = \cdots u_0 \quad u_1 \quad \cdots \quad u$$

$\psi \quad \lambda\bar{\varphi} \quad \varphi_i$

Further, the variable  $\psi$  has the same type as  $\lambda\bar{\varphi}$ , which (say) is of order  $l'$ . It follows that  $l' > l$  and, consequently,  $l' \geq 1$ . By induction hypothesis (i) and (ii),  $s$  has the following shape

$$s = [\cdots \cdots [\cdots \sigma \cdots \underbrace{[\cdots u_1 \cdots u]}_{\text{top 1-stack of } s}] \cdots]$$

top  $(n-l)$ -stack of  $s$ , i.e.  $w$

wherein  $u_1$  has a link to some  $(n-l')$ -stack  $\sigma$ . Since  $l' > l$ , the  $(n-l')$ -stack  $\sigma$  is embedded in the top  $(n-l)$ -stack of  $s$ , as indicated by the figure above. Note that, by induction hypothesis (ii (d)),  $s_\sigma$  computes  $t_{<u_1}$ . In particular the  $top_1$ -element of  $\sigma$  must be  $u_0$ .

Now, to see the structure of  $s'$ , consider the operation  $\delta(u)$ . Let  $w = top_{n-l+1}(s)$ . The operation  $push_{n-l+1}(s)$  pushes a copy of  $w$  on top of  $s$ . The rest of  $\delta(u)$ , namely,

$$pop_1^p; collapse; push_1^{E_i(top_1), n-l+1},$$

affects only the top (duplicate) copy of  $w$ . Applying  $pop_1^p$  to  $push_{n-l+1}(s)$  returns a stack that has  $u_1$  as the  $top_1$ -element; the  $collapse$ -operation then reduces it to a stack that has

a copy  $\sigma'$  (say) of  $\sigma$  as its top  $(n - l')$ -stack, i.e. its  $top_1$ -element is  $u_0$ . The node that is  $push_1$ ed onto the top of the stack at the end of the  $\delta(u)$ -operation (to yield  $s'$ ) is the  $i$ -child of  $u_0$ , which we shall call  $v$ . Observe that the structure of  $\lambda(G)$  implies that  $v$  must then be labelled by  $\lambda\bar{\chi}$  (say) whose type is the same as that of  $\varphi_i$ , i.e. its order is  $l$ . Since  $v$  is linked to the  $(n - l)$ -stack  $w$ , (i) is satisfied.

For (ii), observe that  $t' = tv$ , where  $v$  has an  $i$ -pointer to the distinguished occurrence of  $u_0$ . Note that we then have  $t \xrightarrow{\varepsilon} t'$ . We need to show that  $s'$  computes  $t'$ .

- (a) Observe that  $\ulcorner t' \urcorner = \ulcorner t_{<u_0} \urcorner u_0 v = \ulcorner t_{<u_1} \urcorner v$ . Since  $s_\sigma$  computes  $t_{<u_1}$  (by induction hypothesis (ii (d))), so does  $s'_{\sigma'}$  by Lemma 6.16. Hence,  $\lambda_G(top_2(s')) = \lambda_G(top_2(\sigma'))v = \ulcorner t_{<u_1} \urcorner v = \ulcorner t' \urcorner$ .
- (b) Observe that  $\underline{s}' = \underline{s}v$  (indeed  $v$  has a link and  $collapse(s') = s$ ) and  $\widehat{t}' = \widehat{t}v$ . Thus, by induction hypothesis,  $\underline{s}' = \widehat{t}'$ .
- (c) Since  $s'_{\sigma'}$  computes  $t_{<u_1}$  and  $top_2(s')$  is a copy of  $top_2(s'_{\sigma'})$  augmented with  $v$ , (c) holds.
- (d) We only need to verify (d) for the new link (all other links satisfy (d) because  $s'_{\sigma'}$  computes  $t_{<u_1}$ ). Recall that  $v$  points at the stack  $w$ . Since  $s'_w = s$  and  $t'_{<v} = t$ , (d) holds because  $s$  computes  $t$ .

3. Case  $l = 0$  and  $j = 0$ . Suppose  $u$ 's label is the order-0 variable  $x$ .

By induction hypothesis (ii) and the definition of a traversal (and from  $binder(u)$  being a 0-child),  $t$  must have the following shape:

$$t = \cdots u_0 \overset{0}{\curvearrowright} u_1 \overset{i}{\curvearrowright} \cdots u \underset{\text{@ } \lambda\bar{\varphi}}{\quad} x$$

As in 1.,  $\ulcorner t \urcorner$  has the shape  $\cdots u_0 \underbrace{u_1 \cdots u}_\theta$  and the segment  $\theta$  has length  $p + 1$ , where  $p$  is the span of the variable node  $u$ .

Consider the operation  $\delta(u) = pop_1^{p+1}; push_1^{E_i(top_1)}$ . Applying  $pop_1^{p+1}$  to  $s$  returns a stack that has the @-labelled node  $u_0$  as the  $top_1$ -element. The node that is  $push_1$ ed onto the top of the stack at this point is the  $i$ -child of  $u_0$ , which we call  $v$ . It follows from the structure of  $\lambda(G)$  that  $v$  must be labelled by  $\lambda$ , i.e. its type has order 0. Thus, since  $v$  has no link, (i) follows as required.

For (ii), note that  $t' = tv$ , where  $v$  has a pointer (labelled by  $i$ ) to the occurrence of  $u_0$  indicated above. We have  $t \xrightarrow{\varepsilon} t'$ . We shall show that  $s'$  computes  $t'$ .

- (a) We need to show  $\lambda_G(top_2(s')) = \ulcorner t' \urcorner$ . By definition of  $s'$ , we have  $top_2(s') = (top_2(s))_{\leq u_0} v$ . By induction hypothesis (ii), we have  $\lambda_G(top_2(s)) = \ulcorner t \urcorner$ . Thus

$$\lambda_G(top_2(s')) = \ulcorner t \urcorner_{\leq u_0} v = \ulcorner t_{\leq u_0} \urcorner v = \ulcorner t' \urcorner$$

as required. The second equation follows from the definition of  $\ulcorner \urcorner$  and the fact that  $u_0$  appears in  $\ulcorner t \urcorner$ .

- (b) We have  $\underline{s}' = (\underline{pop_1^{p+1}}(s))v$  and  $\widehat{t}' = \widehat{t}_{\leq u_0} v$ . By induction hypothesis (ii (c)),  $pop_1^{p+1}(s)$  computes  $t_{\leq u_0}$ , in particular  $\underline{pop_1^{p+1}}(s) = \widehat{t}_{\leq u_0}$ . Thus, (b) holds.
- (c) We simply appeal to the induction hypothesis (ii). As before, we need (c) and the fact that  $s$  computes  $t$ .
- (d) Note that no new links have been created in this case, so it suffices to appeal to the induction hypothesis (ii (d)).

4. Case  $l = 0$  and  $j > 0$ . Suppose the label of  $u$  is the order-0 variable  $x$ , which is the  $i$ th item of the list  $\bar{\varphi}$ .



By induction hypothesis (ii) and definition of a traversal,  $t$  has the following shape:

$$t = \cdots u_0 \quad \begin{array}{c} \curvearrowright \\ u_1 \cdots u \\ \psi \quad \lambda\bar{\varphi} \quad x \end{array}$$

Further, the variable  $\psi$  has the same type as  $\lambda\bar{\varphi}$ , which (say) is of order  $l'$ . It follows that  $l' > l$ . By induction hypothesis (i) and (ii),  $s$  has the following shape

$$s = [\cdots \sigma \cdots \underbrace{[\cdots u_1 \cdots u]}_{\text{top 1-stack}} \cdots]$$

wherein  $u_1$  has a link to some  $(n - l')$ -stack  $\sigma$ . Note that, by induction hypothesis (ii (d)),  $s_\sigma$  computes  $t_{<u_1}$ . In particular the  $top_1$ -element of  $\sigma$  must be  $u_0$ .

Now, to understand what  $s'$  looks like, consider the operation  $\delta(u) = pop_1^p; collapse; push_1^{E_i(top_1)}$ . Applying  $pop_1^p$  to  $s$  returns a stack that has  $u_1$  as the  $top_1$ -element; the  $collapse$ -operation then reduces it to a stack that has  $\sigma$  as its top  $(n - l')$ -stack, i.e. its  $top_1$ -element is  $u_0$ . The node that is then  $push_1$ ed onto the top of the stack at the end of the  $\delta(u)$ -operation (to yield  $s'$ ) is the  $i$ -child of  $u_0$ , which we shall call  $v$ . Observe that the structure of  $\lambda(G)$  implies that  $v$  must then be labelled by  $\lambda$ . Since  $v$  does not have a link, (i) is satisfied.

For (ii) let  $t' = tv$ , where  $v$  has an  $i$ -pointer to the distinguished occurrence of  $u_0$ . Then  $t'$  is a traversal such that  $t \xrightarrow{\varepsilon} t'$ . We need to show that  $s'$  computes  $t'$ .

- (a) Observe that  $\ulcorner t' \urcorner = \ulcorner t_{<u_1} \urcorner v$ . Since  $s_\sigma$  computes  $t_{<u_1}$ , we have  $top_2(s') = \ulcorner t_{<u_1} \urcorner v = \ulcorner t' \urcorner$ .
- (b) Observe that  $\underline{s}' = \underline{s}_\sigma v$  and  $\widehat{t}' = \widehat{t}_{<u_1} v$ . Again, since  $s_\sigma$  computes  $t_{<u_1}$ , we have  $\underline{s}_\sigma = \widehat{t}_{<u_1}$  and (b) follows.
- (c) Because  $s_\sigma$  computes  $t_{<u_1}$ , condition (c) holds.
- (d) Again, it suffices to appeal to the fact that  $s_\sigma$  computes  $t_{<u_1}$ , because no new links have been created.

## 7. CONCLUSION

In this paper, we introduced collapsible pushdown automata and proved that they are equi-expressive with (general) recursion schemes for generating trees. This is the first automata-theoretic characterisation of higher-order recursions schemes in full generality.

Due to its length, we decided to restrict this paper to the full proof of the Equi-Expressivity Theorem (that was originally stated in [Hague et al. 2008]). In particular, we had to postpone those questions coming from logic and games. We now briefly discuss the main results in this field as well as other consequences of the Equi-Expressivity.

The Equi-Expressivity Theorem is significant because it acts as a bridge, enabling inter-translation between model-checking problems about trees generated by recursion scheme and model-checking problems/solvability of games on collapsible pushdown graphs. Indeed, consider a  $\mu$ -calculus formula  $\varphi$  and a transition graph  $\text{Graph}(\mathcal{A})$  of a CPDA. Deciding whether  $\varphi$  holds in some vertex  $v$  of the graph is equivalent to decide whether the same formula  $\varphi$  is true at the root of the tree obtained by unfolding  $\text{Graph}(\mathcal{A})$  from  $v$ . As this tree can be obtained as the value tree of some scheme  $G$ , the original question is reduced to decide validity of a  $\mu$ -calculus formula at the root of  $\llbracket G \rrbracket$ . Of course this chain of reductions works in the other direction as well. In particular, the results of [Ong 2006a] imply that  $\mu$ -calculus model-checking is decidable for transition graphs of CPDA.

As  $\mu$ -calculus model-checking for transition graphs of CPDA is equivalent to solving parity games played on transition graphs of CPDA, it was a natural question to study these games

in order to transfer back decidability results to recursion schemes. We showed in [Hague et al. 2008] that those games are decidable (actually they are  $n$ -ExpTime complete for order- $n$  CPDA transition graphs), hence leading to an alternative proof of [Ong 2006a] for the decidability of MSO/ $\mu$ -calculus model-checking for trees generated by recursion schemes.

Later, by carefully studying these games, Broadbent, Carayol, Ong and Serre showed in [Broadbent et al. 2010] that the winning regions of these games admit a finite representation that can later be used (in a non-trivial way and strongly relying on the Equi-Expressivity Theorem) to prove that recursion schemes are constructively reflective with respect to  $\mu$ -calculus and MSO<sup>12</sup>. This result was later subsumed by a result of Carayol and Serre showing that recursion schemes enjoy the effective MSO selection property [Carayol and Serre 2012]. The main tools to prove this result are the equi-expressivity theorem and a careful analysis of the winning strategies of parity games played on transition graph of CPDA.

An important problem on recursion schemes was known as the safe/unsafe conjecture. It asked whether there exists for all schemes, another scheme having the same value tree and verifying the safety constraint. The Equi-Expressivity Theorem permits to rephrase this question as whether CPDA are equi-expressive with higher-order PDA for generating trees. The conjecture was that the former are strictly more expressive. A first step in this direction was obtained by Parys who proved that the Urzyczyn language (which is a language of finite words) is definable by a 2-CPDA but not by any deterministic 2-PDA [Parys 2011]. The proof was obtained by reasoning about accepting runs of 2-PDA, and thus the result on schemes was made possible thanks to the Equi-Expressivity Theorem. Parys recently extended this result by showing that the Urzyczyn language cannot be defined by any deterministic  $n$ -PDA (for any  $n$ ) [Parys 2012].

In [Kartzow and Parys 2012] Kartzow and Parys gave a pumping lemma for collapsible pushdown automata that, thanks to the Equi-Expressivity Theorem, establishes the strictness of the hierarchy  $(RecTree_n\Sigma)_n$  of trees generated by  $n$ -CPDA. More precisely, they gave, for every  $n \geq 0$ , a tree generated by an order- $(n + 1)$  (safe) scheme that no order- $n$  scheme can generate.

The Equi-Expressivity Theorem also opened a new avenue of model-checking algorithms for recursion schemes. The saturation technique – underlying the Moped tool [Schwoon 2002; Suwimonteerabuth et al. 2005; Suwimonteerabuth et al. 2007] for reachability analysis of PDA – was extended in [Broadbent et al. 2012] to CPDA, which led to the implementation of the C-SHORE tool [Broadbent et al. 2013] for recursion scheme model-checking. This automata-theoretic approach contrasted with existing tools (TRecS [Kobayashi 2009a], GTRecS(2) [Kobayashi 2011; Kobayashi 2012], and TravMC [Neatherway et al. 2012]) that are based on intersection type checking. This work also inspired the HorSat tool, which transferred the saturation method to the intersection type setting [Broadbent and Kobayashi 2013]. For completeness, we also mention the recent Preface tool [Ramsay et al. 2014], a fast counter-example abstraction-refinement based model checker for recursion.

Another advantage of the automata-theoretic view on recursion schemes is the transferral of techniques for reasoning about concurrent systems. The search for concurrent extensions of pushdown automata with decidable model-checking properties has been well-studied, and

<sup>12</sup>Let  $\mathcal{R}$  be a class of generators of node-labelled infinite trees, and  $\mathcal{L}$  be a logical language for describing correctness properties of these trees. Given  $R \in \mathcal{R}$  and  $\varphi \in \mathcal{L}$ , we say that  $R_\varphi$  is a  $\varphi$ -reflection of  $R$  just if

- $R$  and  $R_\varphi$  generate the same underlying tree, and
- suppose a node  $u$  of the tree  $\llbracket R \rrbracket$  generated by  $R$  has label  $f$ , then the label of the node  $u$  of  $\llbracket R_\varphi \rrbracket$  is  $f$  if  $u$  in  $\llbracket R \rrbracket$  satisfies  $\varphi$ ; it is  $f$  otherwise.

Thus if  $\llbracket R \rrbracket$  is the computation tree of a program  $R$ , we may regard  $R_\varphi$  as a transform of  $R$  that can internally observe its behaviour against a specification  $\varphi$ . We say that  $\mathcal{R}$  is (constructively) reflective w.r.t.  $\mathcal{L}$  just if there is an algorithm that transforms a given pair  $(R, \varphi)$  to  $R_\varphi$ .

it has been shown that a number of these extensions can be generalised to CPDA [Hague 2013].

We conclude with a brief discussion of further directions:

- (1) Is there an à la Caucal definition for the  $\varepsilon$ -closure of CPDA graphs? As trees generated by  $n$ -CPDA are exactly those obtained by unravelling and unfolding an  $n$ -CPDA graph, is there a class of transformations  $\mathcal{T}$  from trees to graphs such that every  $(n+1)$ -CPDA graph is obtained by applying a  $\mathcal{T}$ -transformation to some tree generated by an  $n$ -CPDA. Note that a  $\mathcal{T}$ -transformation may in general not preserve MSO decidability (as  $n$ -CPDA graphs have undecidable MSO theory [Hague et al. 2008]), but should preserve modal  $\mu$ -calculus decidability of trees generated by  $n$ -CPDA.
- (2) The deepest open problem is without any doubt the equivalence problem for higher-order recursion schemes (i.e. given two schemes, decide whether they have the same value tree). The Equi-Expressivity Theorem implies that the equivalence problem for schemes is interreducible to the problem of decidability of language equivalence between deterministic CPDA (as words acceptors). Of course this problem is extremely hard, as it would generalise the DPDA equivalence decidability result of Sénizergues [Sénizergues 1997; Sénizergues 2002].

#### ACKNOWLEDGMENT

We would like to warmly thank the reviewers for their numerous highly valuable comments.

#### REFERENCES

- Klaus Aehlig. 2006. A Finite Semantics of Simply-Typed Lambda Terms for Infinite Runs of Automata. In *Proceeding of Computer Science Logic (CSL 2006)*, 20th Annual Conference of the EACSL (Lecture Notes in Computer Science), Vol. 4207. Springer-Verlag, 104–118. 1
- Klaus Aehlig, Jolie de Miranda, and C.-H. Luke Ong. 2004. Safety is not a Restriction at Level 2 for string languages. Technical Report PRG-RR-04-023. Oxford University Computing Laboratory. 3.3
- Klaus Aehlig, Jolie de Miranda, and C.-H. Luke Ong. 2005. Safety is not a Restriction at Level 2 for String Languages. In *Proceedings of the 8th International Conference on Foundations of Software Science and Computational Structures (FoSSaCS 2005)* (Lecture Notes in Computer Science), Vol. 3411. Springer-Verlag, 490–501. 1, 1, 3.3
- André Arnold and Damian Niwiński. 2001. Rudiments of  $\mu$ -Calculus. *Studies in Logic and the Foundations of Mathematics*, Vol. 146. Elsevier. 1
- Vince Bárány, Erich Grädel, and Sasha Rubin. 2011. Automata-Based Presentations of Infinite Structures. *London Mathematical Society Lecture Notes Series*, Vol. 379. Cambridge University Press, 1–76. 1
- William Blum. 2017. Type Homogeneity is not a Restriction for Safe Recursion Schemes. CoRR abs/1701.02118 (2017). <https://arxiv.org/abs/1701.02118> 1
- William Blum and C.-H. Luke Ong. 2009. The Safe Lambda Calculus. *Logical Methods in Computer Science* 5, 1 (2009). 2.3
- Christopher Broadbent. 2011. On Collapsible Pushdown Automata, their Graphs and the Power of Links. Ph.D. Dissertation. University of Oxford. 1
- Christopher Broadbent, Arnaud Carayol, C.-H. Luke Ong, and Olivier Serre. 2010. Recursion Schemes and Logical Reflexion. In *Proceedings of the 25th Annual IEEE Symposium on Logic in Computer Science (LICS 2010)*. IEEE Computer Society, 120–129. 1, 7
- Christopher Broadbent and Naoki Kobayashi. 2013. Saturation-Based Model Checking of Higher-Order Recursion Schemes. In *Proceeding of Computer Science Logic (CSL 2013)*, 27th Annual Conference of the EACSL (LIPIcs), Vol. 23. Schloss Dagstuhl – Leibniz-Zentrum für Informatik, 129–148. 7
- Christopher H. Broadbent, Arnaud Carayol, Matthew Hague, and Olivier Serre. 2012. A Saturation Method for Collapsible Pushdown Systems. In *Proceedings 39th International Conference on Automata, Languages, and Programming (ICALP 2012)* (Lecture Notes in Computer Science), Vol. 7392. Springer-Verlag, 165–176. 7
- Christopher H. Broadbent, Arnaud Carayol, Matthew Hague, and Olivier Serre. 2013. C-SHORE: a Collapsible Approach to Higher-Order Verification. In *Proceedings of the 18th ACM SIGPLAN International Conference on Functional Programming (ICFP 2013)*. ACM, 13–24. 7

- Thierry Cachat. 2003. Higher Order Pushdown Automata, the Caucal Hierarchy of Graphs and Parity Games. In Proceedings 30th International Conference on Automata, Languages, and Programming (ICALP 2003) (Lecture Notes in Computer Science), Vol. 2719. Springer-Verlag, 556–569. [1](#)
- Arnaud Carayol, Antoine Meyer, Matthew Hague, C.-H. Luke Ong, and Olivier Serre. 2008. Winning Regions of Higher-Order Pushdown Games. In Proceedings of the 23th Annual IEEE Symposium on Logic in Computer Science (LICS 2008). IEEE Computer Society, 193–204. [1](#)
- Arnaud Carayol and Olivier Serre. 2012. Collapsible Pushdown Automata and Labeled Recursion Schemes: Equivalence, Safety and Effective Selection. In Proceedings of the 27th Annual IEEE Symposium on Logic in Computer Science (LICS 2012). IEEE Computer Society, 165–174. [1](#), [7](#)
- Arnaud Carayol and Stefan Wöhrle. 2003. The Caucal Hierarchy of Infinite Graphs in Terms of Logic and Higher-Order Pushdown Automata. In Proceedings of 23rd IARCS Annual Conference on Foundations of Software Technology and Theoretical Computer Science (FST&TCS 2003) (Lecture Notes in Computer Science), Vol. 2914. Springer-Verlag, 112–123. [1](#)
- Didier Caucal. 2002. On Infinite Terms Having a Decidable Monadic Theory. In Proceedings 27th Symposium, Mathematical Foundations of Computer Science (MFCS 2002) (Lecture Notes in Computer Science), Vol. 2420. Springer-Verlag, 165–176. [1](#), [1](#)
- Alonzo Church and J. Barkley Rosser. 1936. Some Properties of Conversion. *Trans. Amer. Math. Soc.* 39, 3 (mar 1936), 472–472. [2.2](#)
- Bruno Courcelle. 1978a. A Representation of Trees by Languages I. *Theoretical Computer Science* 6 (1978), 255–279. [1](#)
- Bruno Courcelle. 1978b. A Representation of Trees by Languages II. *Theoretical Computer Science* 7 (1978), 25–55. [1](#)
- Bruno Courcelle. 1995. The Monadic Second-Order Logic of Graphs IX: Machines and their Behaviours. *Theoretical Computer Science* 151 (1995), 125–162. [1](#)
- Bruno Courcelle and Maurice Nivat. 1978. The Algebraic Semantics of Recursive Program Schemes. In Proceedings 7th Symposium, Mathematical Foundations of Computer Science (MFCS 1978) (Lecture Notes in Computer Science), Vol. 64. Springer-Verlag, 16–30. [1](#)
- Werner Damm. 1977a. Higher Type Program Schemes and their Tree Languages. In *Theoretical Computer Science, 3rd GI-Conference* (Lecture Notes in Computer Science), Vol. 48. Springer-Verlag, 51–72. [1](#)
- Werner Damm. 1977b. Languages Defined by Higher Type Program Schemes. In Proceedings 4th International Conference on Automata, Languages, and Programming (ICALP 1977) (Lecture Notes in Computer Science), Vol. 52. Springer-Verlag, 164–179. [1](#)
- Werner Damm. 1982. The IO- and OI-Hierarchies. *Theoretical Computer Science* 20 (1982), 95–207. [1](#), [2.3](#), [6](#)
- Werner Damm and Andreas Goerdt. 1986. An Automata-Theoretical Characterization of the OI-Hierarchy. *Information and Computation* 71 (1986), 1–32. [1](#), [6](#)
- Jolie de Miranda. 2006. Structures Generated by Higher-Order Grammars and the Safety Constraint. Ph.D. Dissertation. University of Oxford. [1](#)
- Joost Engelfriet and Erik Meineche Schmidt. 1977. IO and OI. I. *Journal of Computer and System Science* 15, 3 (1977), 328–353. [1](#)
- Joost Engelfriet and Erik Meineche Schmidt. 1978. IO and OI. II. *Journal of Computer and System Science* 16, 1 (1978), 67–99. [1](#)
- Jörg Flum, Erich Grädel, and Thomas Wilke. 2007. *Logic and Automata: History and Perspectives*. Amsterdam University Press. [1](#)
- Stephen Garland and David Luckham. 1973. Program Schemes, Recursion Schemes and Formal Languages. *Journal of Computer and System Science* 7, 2 (1973), 119–160. [1](#)
- Erich Grädel, Wolfgang Thomas, and Thomas Wilke (Eds.). 2002. *Automata, Logics, and Infinite Games: A Guide to Current Research [outcome of a Dagstuhl seminar, February 2001]*. Lecture Notes in Computer Science, Vol. 2500. Springer-Verlag. [1](#)
- Matthew Hague. 2013. Saturation of Concurrent Collapsible Pushdown Systems. In Proceedings of the 33rd International Conference on Foundations of Software Technology and Theoretical Computer Science (FST&TCS 2013) (LIPIcs), Vol. 24. Schloss Dagstuhl – Leibniz-Zentrum für Informatik, 313–325. [7](#)
- Matthew Hague, Andrzej S. Murawski, C.-H. Luke Ong, and Olivier Serre. 2008. Collapsible Pushdown Automata and Recursion Schemes. In Proceedings of the 23th Annual IEEE Symposium on Logic in Computer Science (LICS 2008). IEEE Computer Society, 452–461. [1](#), [1](#), [1](#), [3.9](#), [7](#), [1](#)
- J. Martin E. Hyland and C.-H. Luke Ong. 2000. On Full Abstraction for PCF: I. Models, Observables and the Full Abstraction Problem, II. Dialogue Games and Innocent Strategies, III. A Fully Abstract and Universal Game Model. *Information and Computation* 163 (2000), 285–408. [1](#), [6](#), [9](#)

- Klaus Indermark. 1976. Schemes with Recursion on Higher Types. In Proceedings 5th Symposium, Mathematical Foundations of Computer Science (MFCS 1976) (Lecture Notes in Computer Science), Vol. 45. Springer-Verlag, 352–358. [1](#)
- Alexander Kartzow. 2010. Collapsible Pushdown Graphs of Level 2 are Tree-Automatic. In Proceedings 26th Symposium on Theoretical Aspects of Computer Science (STACS 2010) (LIPIcs), Vol. 5. Schloss Dagstuhl - Leibniz-Zentrum fuer Informatik, 501–512. [3.2](#)
- Alexander Kartzow and Pawel Parys. 2012. Strictness of the Collapsible Pushdown Hierarchy. In Proceedings 37th Symposium, Mathematical Foundations of Computer Science (MFCS 2012) (Lecture Notes in Computer Science), Vol. 7464. Springer-Verlag, 566–577. [7](#)
- Teodor Knapik, Damian Niwiński, and Pawel Urzyczyn. 2001. Deciding Monadic Theories of Hyperalgebraic Trees. In Proceedings of Typed Lambda Calculi and Applications, 5th International Conference (TLCA 2001) (Lecture Notes in Computer Science), Vol. 2044. Springer-Verlag, 253–267. [1](#), [4](#), [4.2](#)
- Teodor Knapik, Damian Niwiński, and Pawel Urzyczyn. 2002. Higher-Order Pushdown Trees Are Easy. In Proceedings of the 5th International Conference on Foundations of Software Science and Computational Structures (FoSSaCS 2002) (Lecture Notes in Computer Science), Vol. 2303. Springer-Verlag, 205–222. [1](#), [1](#), [3](#), [3](#), [3.9](#), [6](#), [6.8](#)
- Teodor Knapik, Damian Niwiński, Pawel Urzyczyn, and Igor Walukiewicz. 2005. Unsafe Grammars and Panic Automata. In Proceedings 32nd International Conference on Automata, Languages, and Programming (ICALP 2005) (Lecture Notes in Computer Science), Vol. 3580. Springer-Verlag, 1450–1461. [1](#), [1](#), [3.2](#), [3.3](#), [4](#), [6.8](#)
- Naoki Kobayashi. 2009a. Model-Checking Higher-Order Functions. In Proceedings of the 11th International ACM SIGPLAN-SIGACT Conference on Principles and Practice of Declarative Programming (PPDP 2009). ACM, 25–36. [7](#)
- Naoki Kobayashi. 2009b. Types and Higher-Order Recursion Schemes for Verification of Higher-Order Programs. In Proceedings of the 36th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL 2009). ACM, 416–428. [1](#)
- Naoki Kobayashi. 2011. A Practical Linear Time Algorithm for Trivial Automata Model Checking of Higher-Order Recursion Schemes. In Proceedings of the 14th International Conference on Foundations of Software Science and Computational Structures (FoSSaCS 2011) (Lecture Notes in Computer Science), Vol. 6604. Springer-Verlag, 260–274. [7](#)
- Naoki Kobayashi. 2012. GTRecS2: A Model Checker for Recursion Schemes Based on Games and Types. A tool available at <http://www-kb.is.s.u-tokyo.ac.jp/~koba/gtrecs2/>. (2012). [7](#)
- Naoki Kobayashi and C.-H. Luke Ong. 2009. A Type System Equivalent to the Modal Mu-Calculus Model Checking of Higher-Order Recursion Schemes. In Proceedings of the 24th Annual IEEE Symposium on Logic in Computer Science (LICS 2009). IEEE Computer Society, 179–188. [1](#)
- Jean-Louis Krivine. 2007. A Call-by-Name Lambda-Calculus Machine. *Higher-Order and Symbolic Computation* 20, 3 (2007), 199–207. [1](#)
- A. N. Maslov. 1974. The Hierarchy of Indexed Languages of an Arbitrary Level. *Soviet Mathematics Doklady* 15 (1974), 1170–1174. [1](#)
- A. N. Maslov. 1976. Multilevel Stack Automata. *Problems of Information Transmission* 12 (1976), 38–43. [1](#)
- Eugenio Moggi. 1989. Notions of Computation and Monads. *Information and Computation* 93 (1989), 55–92. [2](#)
- Robin P. Neatherway, Steven J. Ramsay, and C.-H. Luke Ong. 2012. A Traversal-Based Algorithm for Higher-Order Model Checking. In Proceedings of the 17th ACM SIGPLAN International Conference on Functional Programming (ICFP 2012). ACM, 353–364. [7](#)
- Maurice Nivat. 1972. On the Interpretation of Recursive Program Schemes. In *Symposia Mathematica*. [1](#)
- C.-H. Luke Ong. 2006a. On Model-Checking Trees Generated by Higher-Order Recursion Schemes. In Proceedings of the 21st Annual IEEE Symposium on Logic in Computer Science (LICS 2006). IEEE Computer Society, 81–90. [1](#), [6](#), [6.5](#), [7](#)
- C.-H. Luke Ong. 2006b. On Model-Checking Trees Generated by Higher-Order Recursion Schemes. (2006). Preprint. [6](#), [6.1](#), [6.5](#)
- C.-H. Luke Ong. 2015a. Higher-Order Model Checking: An Overview. In Proceedings of the 30th Annual IEEE Symposium on Logic in Computer Science (LICS 2015). IEEE Computer Society, 1–15. [1](#)
- C.-H. Luke Ong. 2015b. Normalisation by Traversals. CoRR abs/1511.02629 (2015). <http://arxiv.org/abs/1511.02629> [6.1](#)
- Pawel Parys. 2011. Collapse Operation Increases Expressive Power of Deterministic Higher Order Pushdown Automata. In Proceedings 28th Symposium on Theoretical Aspects of Computer Science (STACS 2011) (LIPIcs), Vol. 9. Schloss Dagstuhl - Leibniz-Zentrum fuer Informatik, 603–614. [2.3](#), [7](#)

- Paweł Parys. 2012. On the Significance of the Collapse Operation. In Proceedings of the 27th Annual IEEE Symposium on Logic in Computer Science (LICS 2012). IEEE Computer Society, 521–530. 2,3, 7
- Michael Rabin. 1969. Decidability of Second-Order Theories and Automata on Infinite Trees. *Trans. Amer. Math. Soc.* 141 (1969), 1–35. 1
- Steven J. Ramsay, Robin P. Neatherway, and C.-H. Luke Ong. 2014. A Type-Directed Abstraction Refinement Approach to Higher-Order Model Checking. In Proceedings of the 41st ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages (POPL 2014). ACM, 61–72. 7
- Sylvain Salvati and Igor Walukiewicz. 2011. Krivine Machines and Higher-Order Schemes. In Proceedings 38th International Conference on Automata, Languages, and Programming (ICALP 2011) (Lecture Notes in Computer Science), Vol. 6756. Springer-Verlag, 162–173. 1
- Sylvain Salvati and Igor Walukiewicz. 2012. Recursive Schemes, Krivine Machines, and Collapsible Pushdown Automata. In Proceedings of Reachability Problems - 6th International Workshop (RP 2012) (Lecture Notes in Computer Science), Vol. 7550. Springer-Verlag, 6–20. 1
- Sylvain Salvati and Igor Walukiewicz. 2014. Krivine Machines and Higher-Order Schemes. *Information and Computation* 239 (2014), 340–355. 1
- Sylvain Salvati and Igor Walukiewicz. 2015. Simply Typed Fixpoint Calculus and Collapsible Pushdown Automata. *Mathematical Structures in Computer Science* 26, 7 (2015), 1304–1350. [http://journals.cambridge.org/article\\_S0960129514000590](http://journals.cambridge.org/article_S0960129514000590) 1
- Stefan Schwoon. 2002. Model-Checking Pushdown Systems. Ph.D. Dissertation. Technical University of Munich. 7
- Géraud Sénizergues. 1997. The Equivalence Problem for Deterministic Pushdown Automata is Decidable. In Proceedings 24th International Conference on Automata, Languages, and Programming (ICALP 1997) (Lecture Notes in Computer Science), Vol. 1256. Springer-Verlag, 671–681. 1, 2
- Géraud Sénizergues. 2002.  $L(A)=L(B)$ ? A Simplified Decidability Proof. *Theoretical Computer Science* 281, 1-2 (2002), 555–608. 1, 2
- Colin Stirling. 2005. Higher-Order Matching and Games. In Proceedings of Computer Science Logic (CSL 2005), 19th Annual Conference of the EACSL (Lecture Notes in Computer Science), Vol. 3634. Springer-Verlag, 119–134. 5
- Colin Stirling. 2009. Decidability of Higher-Order Matching. *Logical Methods in Computer Science* 5, 3 (2009). 5
- Dejvuth Suwimonteerabuth, Felix Berger, Stefan Schwoon, and Javier Esparza. 2007. jMoped: A Test Environment for Java Programs. In Proceedings of the 19th International Conference on Computer Aided Verification (CAV 2007) (Lecture Notes in Computer Science), Vol. 4590. Springer-Verlag, 164–167. 7
- Dejvuth Suwimonteerabuth, Javier Esparza, and Stefan Schwoon. 2005. jMoped: A Java Bytecode Checker Based on Moped. In Proceedings of 11th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS 2005) (Lecture Notes in Computer Science), Vol. 3440. Springer-Verlag, 541–545. 7
- Wolfgang Thomas. 1997. Languages, Automata, and Logic. In *Handbook of Formal Language Theory*, G. Rozenberg and A. Salomaa (Eds.). Vol. III. Springer-Verlag, 389–455. 1
- Igor Walukiewicz. 2001. Pushdown Processes: Games and Model-Checking. *Information and Computation* 157 (2001), 234–263. 1

Received Month Year; revised Month Year; accepted Month Year